Keysight Technologies

Improving Network Analyzer Measurements of Frequency-Translating Devices

Application Note



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Introduction

Frequency-translation devices (FTDs) such as mixers, converters, and tuners are critical components in most RF and microwave communication systems. As communication systems adopt more advanced types of modulation, FTD designs are increasingly complex, tests are more stringent with tighter specifications, and the need to reduce costs is more important than ever.

The measurement trade-offs for frequency-translating devices vary widely among different industries. Measurement accuracy, speed, cost and ease of setup are among the considerations for determining the best test equipment. This application note explores current test equipment solutions and techniques that can be used to accurately characterize and test frequency-translating devices. Frequency-translating devices present unique measurement challenges since their input and output frequencies differ. These require different measurement techniques than those used for a linear device such as a filter. This note covers linear frequecy-translation measurements, such as magnitude, relative phase, reflection and isolation. Corresponding accuracy issues are also discussed.

To get the most from this note, you should have a basic under-standing of frequency translation terminology, such as "RF port," "IF port" and "LO port." Understanding of fundamental RF and network analyzer terms such as S-parameters, VSWR, group delay, match, port, full two-port calibration, and test set is also expected. For a better understanding of such terms, a list of reference material appear in the Appendix section.

Network Analyzer Mixer Measurement Configurations

Network analyzers used for testing frequency-translation devices include scalar network analyzers, vector network analyzers with frequency offset capability, and vector network analyzers using an upconversion/down conversion configuration. Each solution has its own advantages and disadvantages. This section provides a synopsis of the three configurations so you can quickly evaluate which is the best fit for your measurement needs. Detailed information about each solution is discussed in later sections.

Scalar network analyzer configuration

The most economical instrument for FTD tests is a scalar network analyzer. A scalar network analyzer uses diode detectors that can detect a very wide band of frequencies. This capability enables a scalar network analyzer to detect signals when the receiver frequency is different from the source frequency. Magnitude-only measurements such as conversion loss, absolute output power, return loss and isolation can be made, as well as nonlinear magnitude measurements such as gain compression. Group delay information is available in some scalar network analyzers using an AM-delay technique, which employs amplitude modulation.

AM-delay measurements are less accurate than group delay measurements obtained with a vector network analyzer. AM delay typically has an uncertainty of around 10 to 20 ns, whereas group delay with a vector network analyzer has an uncertainty as good as 150 ps. Advantages of the scalar solution include low cost and good magnitude accuracy. As shown in Figure 1, fully integrated scalar network analyzers such as the 8711C or 8713C provide economical RF measurements up to 3 GHz, and include AM delay capability. The 8757D scalar network analyzer, shown in Figure 2, measures up to 110 GHz, and provides very good absolute power measurements, particularly when installed with an internal power calibrator and used with precision detectors. In certain cases, such as measuring FTDs with an internal filter, the 8757D with internal power calibrator and vector network analyzer.





Figure 1. 8711C scalar network analyzer configuration

Figure 2. 8757D scalar network analyzer configuration

Vector network analyzer in frequency offset mode configuration

A more versatile solution for FTD test is a vector network analyzer. A vector network analyzer uses a tuned-receiver narrowband detector, which allows measurements of both magnitude and relative phase. The vector network analyzer's frequency offset mode offsets the analyzer's receiver from it's source by a given LO frequency, and makes frequency-translation measurements possible.

There are two common vector network analyzer configurations for FTD measurements. The simplest configuration is shown in Figure 3, and is practical for testing upconverters and downconverters. This configuration allows magnitude-only measurements with a limited dynamic range. For example, if you are interested in the magnitude response of the FTD's passband, the 8753E vector network analyzer has 35 dB of dynamic range in the R channel and provides a quick and easy solution.



To also measure the FTD's relative phase and out-of-band response, Figure 4 illustrates a high-dynamic range configuration. An alternative high-dynamic range configuration can be achieved by splitting the analyzer's RF output power between the device under test (DUT) and the reference mixer. (This configuration is similar to the one shown in Figure 24). In both configurations the vector network analyzer has around 100 dB of dynamic range. A signal into the reference R channel is always necessary for proper phase-locking of the vector network analyzer. In addition, the R channel provides a reference for ratioed measurements such as relative phase or magnitude and phase tracking. Vector network analyzers such as the 8720D series and the 8753E have frequency offset capability to 40 GHz and 6 GHz, respectively.



Figure 4. Vector network analyzer, high dynamic range configuration

The upconversion/downconversion technique

A vector network analyzer in normal operating mode can also be configured for frequency-translation measurements. This configuration has two main advantages. First, the instrument can be used to measure a FTD's magnitude and relative phase response without the need for frequency offset.

As shown in Figure 5, two mixers are used to upconvert and downconvert the signals, ensuring the same frequencies at the network analyzer's source and receiver ports. Second, this configuration provides a potentially more accurate method for measuring absolute group delay. You can simply measure two mixers and halve the response, accepting the resulting uncertainty.



Figure 5. Upconversion/downconversion configuration

Conversion Loss

Definition and importance of conversion loss

Conversion loss, as shown in Figure 6, measures how efficiently a mixer converts energy from one frequency to another. It is defined as the ratio of the output power to the input power at a given LO (local oscillator) power. A specified LO power is necessary because while the conversion loss of a mixer is usually very flat within the frequency span of its intended operation, the average loss will vary with the level of the LO, as the diode impedance changes. As shown in Figure 7, conversion loss is usually measured versus frequency, either the IF frequency (with a fixed LO) or the RF frequency (with a fixed IF). The configuration for a fixed IF measurement is different from those described up to this point. (See the *Fixed IF Measurement* section.) Figure 8 illustrates the importance of a flat conversion-loss response. The DUT is a standard television-channel converter. The input signal consists of a visual carrier, audio carrier and a color subcarrier. Since the frequency response of the converter has a notch in the passband, the color subcarrier is suppressed and the resulting output signal no longer carries a valid color-information signal.



Figure 6. Conversion loss

Figure 7. Two types of conversion loss measurements



Figure 8. TV tuner conversion loss example

Measurement considerations

Conversion-loss measurements can be made with either a scalar network analyzer or a vector network analyzer, using the configurations shown in Figures 1 through 5. The measurement uncertainties are different for each type of analyzer. For both types of analyzers, the two main systematic errors are port mismatch and frequency response. The scalar network analyzer approach requires additional care to minimize errors due to the analyzer's broadband detector. For some vector network analyzers, an internal process, called sampling, and phase-lock requirements can also create errors. Next we will examine each of these error terms and explore techniques to minimize their effects.

Mismatch errors

Mismatch errors result when there is a connection between two ports that have different impedances. Commonly, a device's behavior is characterized within a Z_0 environment, typically having an impedance of 50 or 75 ohms. Although the test ports of a network analyzer are designed to be perfect Z_0 impedances, they are not. The imperfect source and receiver ports of the network analyzer create errors in the calibration stage. Therefore, even before a device under test (DUT) is connected, some errors have already been created in the calibration stage (see Figure 9). Once the DUT is connected, the total measurement uncertainty is equal to the sum of the calibration error plus the measurement error.



Figure 9. Mismatch effects

Once the DUT is connected, interaction between the DUT's ports and the network analyzer's ports cause mismatch errors. As shown in Figure 9, mismatch effects generate three first-order error signals. The first is interaction between the network analyzer's source port and the DUT's input port. The second is between the network analyzer's receiver port and the DUT's output port.

The third is between the network analyzer's source port and receiver port. For an FTD measurement, this third interaction is usually negligible because the conversion loss and isolation of the FTD will attenuate the reflected signals. As frequency translation precludes conventional two-port error correction, attenuators can be used to improve port match.

By adding a high-quality attenuator to a port, the effective port match is improved by up to approximately twice the value of the attenuation. A high-quality attenuator has around 32 dB of port match. The effective match is a function of the quality of the attenuator as well as its attenuation, as shown in Figure 10.

As shown in Figure 11 and Figure 12, a well-matched attenuator can significantly improve the effective port match. For example, a 10-dB attenuator, with a port match of 32 dB, can transform an original port match of 10 dB into an effective match of 25 dB. However, as the match of the attenuator approaches the match of the original source, the improvement diminishes. As shown in Figure 12, the larger the attenuation, the more nearly the resulting match approaches that of the attenuator. However, excessive attenuation is not desired since this will decrease the dynamic range of the measurement system. The port match of an FTD can be poor, typically around 14 dB. Therefore, it is recommended that attenuators be placed at the FTD's input and output ports. Scalar network analyzers use different detection methods than vector network analyzers that should be considered when testing FTDs.



Figure 10. Effective match as a function of attenuator's match

Figure 11. Effective match as a function of attenuator's match (fixed 10 dB attenuator)



Figure 12. Effective match as a function of attenuation (attenuator match = 32 dB)

Considerations unique to the scalar network analyzer

Scalar network analyzers use broadband diode detectors. Although capable of both narrowband and broadband detection, the 8711 series, which includes the 8712C and 8714C vector network analyzers, uses broadband detection for FTD measurements. Therefore, if you use an 8712 or 8714, use the same FTD test considerations as you would for a scalar network analyzer.

Importance of proper filtering A scalar network analyzer's broadband diode detector will detect any signal that falls within its passband. Although a broadband diode detector is an economical way to measure FTDs, it also can allow certain detection errors. The diode detector will detect the desired IF signal, as well as other mixing products or spurious signals. To minimize the detection of undesired signals, a filter should be placed at the detector port to pass the desired IF signal but reject all other signals. Figure 13 shows an example of the incorrect measurements that might result when improper IF filtering is used in a scalar network analyzer configuration.

In Figure 13, the conversion loss measurement without the IF filter appears to be better than it really is. The lack of an IF filter generates erroneous results. The broadband diode detector cannot discriminate the frequency of the received signal(s) – it measures the composite response. If the source is set at 1 GHz, it is "assumed" that this is the frequency of the detected signal. Any signal that falls within the passband of the diode detector will be detected. If the output of a DUT is composed of the desired IF signal plus the image frequency, LO and RF feedthrough and other spurious signals, the diode detector will detect the composite of all the signals within its passband. This composite signal will be incorrectly displayed as a response that occurs at 1 GHz.



Figure 13. Conversion loss response with and without an IF filter

Frequency response error Without performing any sort of calibration on a scalar or vector network analyzer, the frequency response of the test system cannot be separated from the FTD's response. One way to correct these errors is to perform a frequency-response normalization or calibration, using a through connection in place of the DUT.

For scalar network analyzers such as the 8757D, which very accurately measures absolute power, the normalization calibration can be performed in two steps. See Figure 21. First, the absolute RF power is measured and stored in memory. Second, the DUT is inserted and the absolute IF power is measured. Conversion loss is displayed using the Data/Memory format. The conversion loss value is very accurate since the measurements of the two absolute power levels, RF and IF, are very accurate. Ratioing two very accurate absolute power levels removes the frequency response error. In some cases, a scalar 8757D with an internal power calibrator and precision detector can make more accurate conversion loss measurements than a vector network analyzer. In the Accuracy *Comparison of the 8757D and a Vector Network Analyzer* section, error terms are used to illustrate how a scalar analyzer with internal power calibrator can be more accurate than a vector network analyzer.

For analyzers that do not precisely measure absolute power, corrections for the frequency response error are less accurate. The input and output of the DUT are at different frequencies, but the normalization can only be performed over one frequency range. The result is that part of the test system is characterized over a different frequency range than that which is used during the actual measurement.

There are two choices for the frequency range used for the normalization: either the DUT's input (RF source) range, or the DUT's output (receiver) range. The normalization should be done to correct the portion of the test system that contributes the largest uncertainty; for example, this would be the portion with the most loss or frequency roll-off. Systems and components tend to have poorer performance at the higher frequencies, therefore the calibration should normally be performed at the higher frequencies. In general, high-quality, low-loss cables and connectors should be used to minimize frequency-response errors.

For higher accuracy, combine a normalization calibration with external error-term correction. During the normalization, only one section of the test configuration should be connected, either the DUT's input range or the DUT's output range. For highest accuracy, the removed section can be characterized separately. An external computer is used to extract the removed section's S-parameters from the network analyzer. This data is then used to modify the network analyzer's error terms to account for the effects of the removed section.

Considerations unique to the vector network analyzer

Now that we have covered the important measurement considerations of the scalar network analyzer, let's continue with a discussion of the vector network analyzer. The important considerations include: the need for proper filtering, an accurate and stable LO, and power meter calibration for the most accurate measurements.

Importance of proper filtering A vector network analyzer has a narrowband tuned receiver. Since the received signal is heavily filtered by an internal narrowband IF filter, broadband detection issues encountered by the scalar network analyzer are not present. However, proper filtering is still very important for vector network analyzers with sampler-based receivers, such as the 8753E and the 8720D.

Sampling architecture and issues A sampler-based receiver consists of a voltage-tunable oscillator (VTO), a pulse generator, and a sampler (switch). The VTO drives the pulse generator, which in turn drives the sampler. As a result, with proper tuning of the VTO, this combination replicates a down-converted input signal at the correct intermediate frequency (IF) for further processing. This combination is similar to a harmonic mixer in which the harmonics of the LO are generated in the mixer, and the input signal can mix with any harmonic. With proper tuning of the LO, one of the LO harmonics is offset from the input signal to produce the correct IF signal.

Since there are many LO harmonics, any signal (desired or not) that is one IF away from any of the LO harmonics will be downconverted to the network analyzer's IF and detected. To illustrate this sampler effect, let's use the 8753E as an example. The IF of the 8753E vector network analyzer is 1 MHz. Errors might result because the incoming signal is not filtered until after it is downconverted to the IF. If there is only one signal at the receiver, this signal will mix with one LO harmonic and is properly downconvert to 1 MHz. However, if there are multiple signals that are 1 MHz away from any of the LO harmonics, these signals will be downconverted to 1 MHz, which creates erroneous responses.

Figure 14 illustrates an example of this sampler effect where the desired IF output signal of the mixer is 110 MHz. In order to correctly detect this signal, the 8753E will use a VTO of 54.5 MHz, where its second harmonic (109 MHz) will properly downconvert 110 MHz to the desired 1 MHz IF signal. In the illustration, we show two mixer products (6 LO-2RF and 9 LO-RF) that would also produce IFs at 1 MHz. Notice that these two spurs occur on either side of the LO harmonics (18 VTO and 42 VTO, respectively), but as long as they are 1 MHz away, they will be downconverted to 1 MHz. Aside from the signals which downconvert to 1 MHz, signals that will directly pass through the finite passband of the 1 MHz bandpass filter can cause problems. In the 8753E, IF BWs from 10 Hz up to 6 kHz are available.

As shown in Figure 15, wide IF BW allows any signal that falls within the passband to be detected. For this reason narrow IF BWs are recommended. However, narrower IF BWs result in slower measurement speed.

Several techniques can significantly minimize spurious responses. One technique is to use adequate filtering at the network analyzer's receiver port. Another technique, as illustrated in Figure 15, is to reduce the instrument's IF bandwidth. Reducing the IF bandwidth will more selectively filter signals in the instrument's IF signal path. A third technique is to avoid frequency spacings equal to the IF. In the case of the 8753E, the IF is 1 MHz. Therefore 1 MHz and multiples thereof should be avoided when choosing frequencies and frequency spacings. A fourth technique is to avoid or eliminate certain frequencies that might cause spurious responses. A spur prediction program, available in the 8753-2 product note, predicts the frequencies that might cause spurious responses. Modifying the frequency selection or use the analyzer's frequency list mode, in which the internal source will step from one continuous wave (CW) signal to the next, to avoid these frequencies. This program, written in BASIC 5.0, is customized for the 8753 series and swept IF mixer measurements, although it can be modified for other measurements such as fixed IF. This program only predicts the possible occurrence of a spur — it does not predict its power levels, and it does not consider RF and LO subharmonics.





Figure 14. Diagram of spurious measurement responses

Figure 15. Effects of IF BW on spurious measurement responses

R-channel phase-locking considerations For FTD tests, using frequency offset mode with a vector network analyzer, many times you are required to remove the R-channel jumper and input a signal into the R Ref-In port. Once an external signal is introduced into the R channel, proper filtering at the Ref-In port is essential. Certain requirements must be met in order for the R channel to phaselock properly. First, the signal into the R channel must be within a specified power level throughout the entire IF range. For example, in the 8753E vector network analyzer, the R-channel signal must be between -35 dBm and 0 dBm throughout the selected IF range, otherwise proper phase lock will not occur. If the R-channel filter does not cover the entire IF range, modifying the selection of the IF range accordingly will eliminate the phase-lock error. For example, if the IF range is set from 10 MHz to 300 MHz, but the R-channel filter is already rolling off at 300 MHz, then change the IF stop frequency to around 250 MHz, or use a different filter. The second requirement is that the R-channel sampler needs to phaselock with the correct signal. If there are spurious signals entering the R-channel sampler, it might try to establish phase lock with an improper frequency. This situation can be avoided with proper R-channel filtering. (If you are using the 8712C or 8714C vector network analyzers, R-channel considerations are irrelevant because broadband detectors are used.)

LO accuracy and stability For the vector network analyzer, the use of an accurate, stable LO is required for accurate magnitude and relative phase measurements. As shown in Figure 3, a network analyzer in the frequency offset mode will automatically set its internal source to sweep over the corresponding RF range once you have entered the necessary information (i.e., IF range, LO information, upconversion or downconversion, RF <> LO). Therefore, if the LO source is not accurate, a network analyzer will not receive the anticipated IF signal. As a result, the IF signal can fall on the skirts of the IF filter or worse, it can fall entirely outside of the filter passband. In the first case, this inaccuracy is transferred to the measurement results, and in both cases, phase lock can possibly not even occur.

This LO accuracy and stability requirement can be explained with the help of Figure 16, which illustrates the block diagram of the 8753E in frequency offset mode. At the start of the sweep, the RF source is pretuned to the IF frequency plus the LO offset, then the main phase-lock loop (PLL) is locked up. The receiver will sweep over the IF range, and the source will track it with the fixed offset. As shown, the mixer's IF signal is downconverted in the R-channel sampler to provide the 1MHz phase-lock signal. Therefore, the LO source must be stable and accurate in order to provide an LO signal that will properly convert the mixer's RF input to its desired IF output. This mixer's IF signal, once down-converted by the sampler, is compared to an internal 1-MHz reference signal where a resulting voltage, proportional to their phase difference, is used to fine-tune the RF source. If the mixer's IF signal is too far from the expected receiver's frequency, then it might lie outside the acquisition range of the PLL. The phase-lock algorithm will not work and a phase-lock error is displayed. For example, the 8753E requires a LO frequency accuracy of within a few kHz of the nominal frequency.



Figure 16. 8753E block diagram of frequency offset mode

Power meter calibration Some vector network analyzers, such as the 8753E and the 8720D, are capable of using power meters to enhance their source and receiver accuracy. This power-meter calibration can significantly minimize frequency response error, as well as correct for some mismatch effects. The following discussion relates to a down-converter measurement, although the same concepts also apply for upconverters. It is a three-step process:

- 1. Perform power-meter calibration over the IF range.
- 2. Perform response calibration over the IF range.
- 3. Perform power-meter calibration over the RF range.

The power-meter calibration procedure needs to be done correctly or it can lead to unexpected errors. Let's look at each step for performing a power-meter calibration.

Step 1. Perform power-meter calibration over the IF range.

The purpose of this step is to calibrate the network analyzer's source for a very accurate power level over the IF range. After this, a response calibration completely corrects the receiver's frequency response over the IF range. With the power-meter calibration, the accuracy of the power meter is transferred to the network analyzer.

First, the power meter is preset and zeroed. The network analyzer is set to sweep over the IF range.

Before connecting the equipment as shown in Figure 17, you need to ensure that the R channel will phase lock properly. For network analyzers, such as the 8753E, that do not have an internal/external switch for the R channel, you must turn on the frequency offset before disconnecting the R-channel jumper. This step is important since it prevents the network analyzer from attempting to do a "pretune calibration" when there is no valid R-channel signal. If the network analyzer attempts a pretune calibration without a valid R-channel signal, the pretune constants would be invalid.



Figure 17. Configuration for power meter calibration over the IF range

Once the pretune constants are erroneous, proper phase lock will not occur even with a valid R-channel signal. If this should happen, reconnect the R-channel jumper and allow the automatic pretune calibration to fix itself. This step is not necessary for an 8720D with Option 089 which provides an internal/external R-channel switch.

At this point, the LO frequency should be at 0 Hz, so that the source and receiver are at the same frequency. Connect the equipment as shown in Figure 17. The power at point A and B are assumed to be the same, but actually are not. Therefore, it is important to make points A and B as symmetrical as possible. For example, if point B is connected to one arm of the power splitter, then ideally the power sensor should also be connected directly to the other arm of the power splitter (that is, without any additional accessories such as an adapter or cable). Figure 17 is configured so that the mixer's IF port will eventually be connected to point B.

A 0 dBm power level is optimal. Power meters are most accurate at 0 dBm because they use a precision 50-MHz 0-dBm source as their reference. If the frequency response table of the power meter is used and the measured power is close to 0 dBm, very high measurement accuracy can be achieved (typically better than 0.05 dB). However, point B differs from point A by the tracking error of the splitter and this adds approximately another 0.2 dB of uncertainty. Select the network analyzer's source power so that point A and B will be 0 dBm.

Set the power-meter calibration power level. The number of calibration points should be set at this time. Using no more than 26 or 51 points usually yield very good results. The number of points can then be increased before calibrating the network analyzer's receiver. The power-meter calibration constants will be interpolated over the new points. Another time-saving technique is to use one receiver calibration for different frequency ranges. This is accomplished by calibrating over the largest frequency span, then turning calibration interpolation on and changing the span and number of points. Interpolation errors are quite small, usually adding less than 0.05 dB to the total uncertainty. During the power-meter calibration, it is important to connect the equipment as shown in Figure 17. If point B is unterminated, errors will result due to reflections at this port. Terminate point B with the components within the IF path, such as the attenuator, filter and cabling. This closely reproduces the same condition during the calibration as in the transmission measurement. With the components connected, the power-meter calibration will account for mismatches due to these components (such mismatches contribute to unleveled power levels at both points A and B). An alternative technique would be to terminate point B with a good Z_n termination.

During the power meter calibration, via the GPIB interface, the network analyzer will automatically read the output from the power meter at each frequency point and adjust its source power exactly by successively resetting and measuring the power. In most instances, two readings are sufficient to settle to within 0.02 dB of the required power at point A.

Step 2. Perform response calibration over IF range.

With a very accurate power level at point B, now calibrate the network analyzer's receiver for accurate absolute power measurements over the IF range.

The equipment is still connected as shown in Figure 17. As mentioned earlier, the power into the R channel must be within the correct power range (–35 to 0 dBm, in the case of an 8753E), throughout the selected IF range, or proper phase lock will not occur.

Examine the R-channel response. The values displayed for the R channel can appear surprising. Notice that the values displayed are positively offset by approximately 16 dB. For example, with a signal of 0 dBm, the R channel will display approximately +16 dBm. Normal operation usually displays ratioed measurements (A/R or B/R). A mathematical offset is used to account for the differences between the R-channel path and the A-or B-channel path. The network analyzer cannot discern that the signal is directly applied to the R sampler, so the signal level is displayed approximately 16 dB too high. This offset and the frequency-response error are accounted for after a response calibration is performed on the R channel.

Go to the calibration menu and perform a response calibration. The through standard in Figure 17 is composed of the attenuator, filter, and cabling. After the response calibration, the R channel will indicate 0 dBm. At this point, the accuracy of the power meter has been transferred to the network analyzer's receiver at point B. In addition, the analyzer has accounted for the frequency response of the through standard. Point B is now calibrated for a very accurate 0 dBm absolute power reading.

Step 3. Perform power-meter calibration for RF range.

This last step, another power-meter calibration over the RF range, provides a very accurate power level at the RF port of the mixer.

An alternative is to do one power-meter calibration for both the RF and IF ranges. As mentioned earlier, power-meter calibration interpolation is a useful, time-saving tool. To save time, the first power-meter calibration can be performed over a frequency range that encompasses both the RF and IF range. Then turn on interpolation to preserve the calibration in the separate RF and IF ranges. This does compromise accuracy to some extent.

Connect the equipment as shown in Figure 18. Once the appropriate information such as the LO information, upconversion/downconversion, and RF <> LO have been set, the network analyzer will automatically set the RF range. After selecting the desired calibration power at port B, perform the power-meter calibration.



Figure 18. Configuration for power-meter calibration over the RF range

Upon completion of the power-meter calibration, a very accurate reading of the DUT's absolute output power is displayed on the R channel, which has been calibrated to 0 dBm. Notice that the reading is not necessarily the conversion loss of the DUT; it is the absolute power reading of the DUT's output power. For example, if the DUT has a 6 dB conversion loss when stimulated with -10 dBm of RF input power, the R channel will display -16 dBm. If you want the R channel to display the conversion loss directly, then in Step 1 select the power at point B to be the RF drive power for the DUT's RF port. The disadvantage here is some loss in accuracy; power meter calibration is most accurate at 0 dBm power level.

Power-meter calibration for a high-dynamic-range measurement For a

high-dynamic-range configuration, Figure 19 is analogous to Figure 17 above. Notice that the R-channel jumper is connected, since a valid signal is required into the R channel for phase locking. In this configuration, the R channel is used only for the purpose of phase locking. It is not included as a direct path in the actual measurement. This is the high-dynamic-range configuration you should use for performing Steps 1 and 2.



Figure 19. Power meter calibration over the IF range, high-dynamic-range configuration

Figure 20 is analogous to Figure 18. This high-dynamic-range configuration is useful for devices that includes a filter, as shown. A filter is especially important in the R channel to suppress spurious signals that can cause false phase locking. A filter into the B channel might not be necessary. This is the configuration you would use for performing Step 3.



Figure 20. Power meter calibration over the RF range, high-dynamic-range configuration

Accuracy comparison of the 8757D and a vector network analyzer

In some cases, an 8757D with an internal power calibrator and precision detectors will make more accurate conversion loss measurements than a vector network analyzer with power meter calibration. To illustrate the difference in accuracy, let's look at the error terms involved in a downconverter measurement in either case.

In Figure 21a, the source is set to the RF range. The precision detector very accurately detects the absolute power at its port over the RF range. The resulting errors are m_{η} , the mismatch error, and $r_{detector RF}$, the uncertainty in the detector's calibration over the RF range. The $r_{detector RF}$ error is very small, approximately ± 0.18 dB.

Next, the RF response is stored into memory. The purpose of this step is to correct for the frequency response of the attenuator and the source, $r_{source,RF}$.

In Figure 21b, the DUT is connected. The errors consist of mismatch errors, m_2 and m_3 , and the uncertainty of the detector's calibration over the IF range, $r_{detector IF}$. The latter error is very small. Notice that an attenuator is not used at the output of the DUT since the precision detector measures most accurately at its port. If additional devices, such as an attenuator or filter, are added, their effects will add additional calibration steps and therefore added uncertainty. This configuration is especially advantageous for FTDs with internal filters.

In this 8757D calibration and measurement, the total uncertainty is equal to the sum of three uncorrected mismatch error terms, m_1 , m_2 , and m_3 . The detectors errors are very small and are negligible.

With the vector network analyzer with power-meter calibration, five mismatch errors result. Let's take a closer look at the error terms involved in a vector network analyzer with power meter calibration measurement.





(a) Absolute power measurement over the RF range

(b) Absolute power measurement over the IF range

In Figure 22a, the source is set to the IF range. A power meter calibration is performed. The errors consist of the mismatch error, m₁, and the source frequency response error over the IF range, rsource IF. The latter error is small.

In Figure 22b, a normalization calibration is performed. Errors = $m_2 + r_{receiver IF}$, where $m_2 = m_1 + r_{detector IF}$ and receiver IF is the receiver frequency response error over the IF range.

In Figure 22c, the source is set to the RF range and a power meter calibration is performed. Errors = $m_3 + r_{source RF}$ where $r_{source RF}$ is the source frequency response error over the RF range.

In Figure 22d, the DUT is connected. Mismatch errors, m_4 and m_5 , are generated. As illustrated, the total uncertainty is due to five uncorrected mismatch error terms, m_1 , m_2 , m_3 , m_4 , m_5 . The other errors are relatively small.

In summary, the 8757D with its internal power calibrator and precision detectors can be more accurate than a vector network analyzer with power meter calibration since there are less connections, resulting in less mismatch effects and therefore less measurement uncertainty. However, the vector network analyzer is capable of further error correction using error-term manipulations with an external computer. With additional error correction, the vector network analyzer might be the more accurate instrument. In the section, *Absolute Group Delay – A More Accurate, Lower Ripple Technique*, procedures for external error-term manipulations are discussed.



Figure 22.

(a) Power meter calibration over the IF range
(b) Normalization calibration over the IF range
(c) Power meter calibration over the RF range
(d) DUT is measured

Fixed IF measurements

When a communication system receives a signal using a superheterodyne receiver, the information is downconverted to an appropriate frequency called the intermediate frequency. The components in the IF section of the receiver usually provide most of the gain and frequency selectivity of the communication system. Using a fixed IF allows a designer to optimize the IF filtering and amplification which yield the highest performance. Typical IF frequencies for FM radios are 10.7 and 21.4 MHz, radar receivers are 30 and 70 MHz and satellite receivers are 70 and 140 MHz.

The other way in which conversion loss or gain flatness can be measured is by keeping the IF at a constant frequency. This is known as a fixed-IF measurement. In order to accomplish this, the LO must sweep in conjunction with the RF input signal, keeping a constant frequency offset equal to that of the IF. In many cases, this measurement more closely matches the operation of the DUT in the actual application.

The most common way to perform this measurement is to use the vector network analyzer in a tuned-receiver mode (without frequency offset). Figure 23 illustrates this configuration. As shown, two external sources are used. One external source provides the RF signal and the other provides the LO signal. Both sources sweep in a stepped-frequency mode under the direction of a measurement controller via the GPIB bus. This measurement is an ideal candidate for automation using the built-in test-sequencing or IBASIC capability of many modern network analyzers. Also, notice that an external reference must be connected between the VNA and the sources (i.e. a 10 MHz reference). When testing a tuner with a fixed-IF sweep, the controller must be able to tune the internal LO. If an analog voltage is used, then a programmable power supply or digital-to-analog converter is needed. If the LO is tuned digitally, the proper interface must be used.



Figure 23. Fixed IF configuration

Testing a mixer under fixed IF conditions can also be accomplished with one external source and the network analyzer under the control of an external computer. The configuration is very similar to Figure 23, except there is only one external LO source and a computer interface for both the network analyzer and the external LO source. The configuration is shown in Appendix C.

Two cases should be considered for a fixed IF measurement. In both, the LO and RF frequencies are stepped over their respective ranges but in each case the network analyzer is configured slightly differently.

The first considers the FTD as a downconverter, where the RF output frequency of the analyzer is stepped over the RF range and the analyzer measures a fixed IF frequency at the R-channel input. The second uses the FTD as an upconverter and the output frequency of the analyzer (test port 1) is fixed at the IF frequency. The analyzer must measure the stepped RF frequency at the R-channel input. Both measurements are accomplished using the frequency offset mode capability available on many modern network analyzers. This tuned receiver mode allows measurement to be made at frequencies other than that of the analyzer's internal source.

The *Appendix* provides a program for a fixed IF measurement on a mixer that operates as a downconverter. This Basic for Windows program uses the 8753E as the RF source and receiver and the LO signal is provided by a ESG-D3000A signal generator. The LO source commands are written in SCPI, so any compliant signal generator will work. The program sets an absolute power level to the mixer's RF input port and measures the absolute power level at the IF port. The analyzer's receiver is first calibrated at the fixed IF frequency, then the analyzer's RF source level is set. The difference between the two signals is the conversion loss of the mixer under test. The program assumes the RF drive level from the test port remains constant over the stepped frequency range and also does not include insertion loss from the test port cable. The accuracy of the measurement can be enhanced by including a power-meter calibration at the IF and RF ranges as detailed in the 8753E Operating Manual. Care must be taken to ensure adequate signal level to the R-channel input or the system will not phase lock to the IF signal. The R-channel input requires a signal level between 0 dBm and -35 dBm. Adequate filtering is also required to prevent spurious response from entering the receiver.

Relative Phase Measurements

Measuring mixer relative phase parameters such as tracking, relative phase linearity and group delay requires a vector network analyzer. Relative phase between two mixers can be measured, but the absolute phase response of a single mixer cannot. In order to make a phase measurement of a FTD, a second mixer is needed to provide a reference phase signal. This reference mixer is needed because when the analyzer is in frequency-offset mode, the source and receiver function at different frequencies and phase is not defined between two different frequencies. This reference mixer needs to be driven by the same RF and LO signals that are used to drive the mixer under test. The IF output from the reference mixer is applied to the reference phase-lock R channel of the vector network analyzer.

Relative phase and magnitude tracking

The capability to measure the amplitude and relative phase match between frequency-translation devices such as mixers is increasing in importance as the number of multichannel signal processing systems increases. These multichannel systems, such as direction-finding radars, require that the signal transmission through each channel be amplitude- and phase-matched. To achieve the required match between channels, each channel usually is manufactured with matched sets of components.

The match between FTDs is defined as the difference in amplitude and relative phase response over a specified frequency range. Also, the tracking between FTDs is measured by how well the devices are matched over a specified interval after the removal of any fixed offset. For example, this interval can be a frequency interval or a temperature interval, or a combination of both.

The configuration shown in Figure 24 can be used to make magnitude and relative phase matching measurements. First, one DUT is measured and its response is stored in memory. Then the second DUT is measured and the analyzer's data/memory feature is used to compare them. The analyzer accounts for the response of the test system since the two DUTs are measured with exactly the same conditions. Figure 25 shows the magnitude and relative phase match between the two DUTs.



Figure 24. Mixer matching - magnitude and group delay



Figure 25. Tracking responses

If both DUTs must be measured simultaneously (for tuning), then you can place one DUT in the B channel and one in the R channel. However, in this configuration you are limited by the dynamic range of the R channel. Therefore, the DUT in the R-channel path should not have a filter. If it does, you can only view the DUT's passband match. For two DUTs with internal filters, you cannot simultaneously view the response using the configuration in Figure 24.

Figure 26 illustrates a configuration that takes advantage of the high dynamic range of the A and B channels. The analyzer compares channel A and B and can be used to actively display A/B responses, or for making high-dynamic-range measurements such as tracking of out-of-band responses. In this case, the R channel is used only for phase locking.



Figure 26. Simultaneous tuning and high-dynamic range configuration

Group delay

Group delay is a measure of a signal's transition time through a device. It is classically defined as the negative derivative of phase vs. frequency. Relative group delay measurements can be made with the same setup as that shown for relative phase measurements.

Important parameters when specifying group delay

The result of a group-delay measurement depends upon many measurement factors, including aperture, averaging and IF bandwidth. The true negative derivative of phase vs. frequency cannot be determined because any measurement made with a network analyzer is discrete, with one phase point per frequency point, so there is no continuous function from which to take a derivative. Instead we calculate the slope of the phase over a frequency range. We use the values of two frequency points on either side of the frequency of interest and calculate the corresponding phase slope. As illustrated in Figure 27, this method can yield a result that is different than the true derivative of the phase.

The number of frequency points over which the slope is calculated is the aperture. The default aperture is the smallest difference between any two frequency points. The aperture can be adjusted via the "smoothing aperture" control to use a wider frequency range to calculate the phase slope. The maximum aperture is limited to 20% of the frequency span of the measurement.



Figure 27. Group delay

The aperture determines the information that is transferred from one response to the other. Any effects on the phase response carry over directly to the group-delay response. For example, noise in the phase response directly translates into noise in a group delay response.

For example, if you set a narrow aperture, the noise in the phase response becomes more significant, making the group-delay response noisier. At the same time, a narrow aperture allows you to see variations in the group-delay response, which you would miss otherwise. But, if you increase the smoothing aperture too much, you begin to lose resolution since the peaks start to disappear. Figure 28 illustrates how dramatic the trade-off between resolution and noise as a function of aperture can be.



Figure 28. Group delay as a function of aperture

If you do not want to trade off resolution and noise, you can achieve a balance by trading off some measurement speed. Using averaging or narrowing the IF bandwidth results in slower measurement speeds without degrading resolution or noise.

The smoothing aperture should always be specified for a group delay measurement. A group delay measurement is only truly valid if its aperture is specified. Figure 28 illustrates how dramatically different the group delay responses can be, depending on the smoothing aperture settings. Always specify the smoothing aperture when making a group delay measurement. If you do not want to trade off resolution and noise, you can achieve a balance by trading off some measurement speed. Using averaging or narrowing the IF bandwidth results in slower measurement speeds, without degrading resolution or noise.

Absolute group delay

If you are interested in absolute group-delay measurements a calibration mixer is required. When measuring the group delay of a linear device, standard vector error correction calls for a through connection (delay=0) to be used as a calibration standard. The solution to the problem of measuring the delay of a nonlinear device like a mixer is to use a calibration mixer with very small group delay as the calibration standard. There are several techniques that can be used to create a calibration mixer with known group delay. Two mixers have been characterized by Keysight Technologies, Inc. for this purpose:

Mixer	ANZAC MDC-123 MCL ZFM-4
Frequency range	30 MHz to 3,000 MHz dc to 1,250 MHz
Group delay	0.5 ns 0.6 ns

Upconversion/downconversion

The group-delay values above were obtained by measuring two mixers, using the upconversion/downconversion configuration shown in Figure 5. The measured group delay was very consistent, with flat frequency response. For example, a pair of MDC-123 mixers was measured, resulting in 1 ns of group delay excluding the effects of the IF filter and components placed between the mixer pair. A single mixer, then, must have a group delay between 0 and 1 ns. Therefore, one can state that the group delay of the MDC-123 is 0.5 ns, with an accuracy of \pm 0.5 ns. This is a very conservative uncertainty window. Therefore, the uncertainties of both mixers are equal to their respective group delay, although these are worse-case estimates. After the through calibration you are ready to test the frequency-translating DUT, enter a value of electrical delay to compensate for the group delay of the calibration mixer (-0.5 ns for the MDC-123 or -0.6 ns for the ZFM-4). The resulting accuracy derived from this technique is typically the group-delay uncertainty of the calibration mixer.

Choosing a calibration mixer with even smaller absolute group delay will further improve the measurement uncertainty because the worst case error is half the stated value.

Another important consideration to remember when selecting a calibration mixer is that the delay of the device must have a relatively flat group delay response over frequency. The convention is to select a mixer with very wide bandwidth compared to the mixer to be tested – the wider the bandwidth, the better the mixers' delay linearity will be. For further information regarding the desirable characteristics of a calibration mixer, see the Appendix section, *Calibration Mixer Attributes*. When using the calibration mixer in the test system, typically only a response calibration is used to cal through the mixer. The mismatch interaction between the test system and the cal mixer and subsequent measurements with the DUT will introduce uncertainty in the form of ripple into the measurement. Adding attenuation before and after each mixer can greatly improve this ripple.

Another technique for improving the measurement uncertainty in absolute group delay and delay linearity uses an upconversion/downconversion with additional error correction at the mixer's test ports. The technique is discussed in the section, *Absolute Group Delay* – *A More Accurate, Lower Ripple Technique*.

Modulation delay

Measuring the group delay of converters and tuners with inaccessible internal LOs is very difficult using the method described above because we lack the necessary phase-coherent LO for phase locking. One alternate method uses a definition of group delay as the time delay through the mixer of a modulated signal. This technique can measure group delay on devices without needing a reference mixer. Using a frequency-response normalization, absolute and relative group delay can be measured.

As shown in Figure 29, the modulation-delay method for measuring group delay is accomplished by modulating an RF carrier. AM, FM or pulse modulation can be used. The carrier is swept through the frequency-translating device, the IF output is demodulated, and finally the phase of the demodulated signal is compared with the original baseband modulation tone (which can be derived by demodulating the carrier at the input to the DUT). If square wave modulation is used, zero-crossings can provide the delay detection mechanism; if sine wave modulation is used, the phases of the sine waves must be compared. One main drawback for the AM-delay technique is that it relies on the device not having amplitude limiting, which would strip off the amplitude modulation.



Figure 29. Modulation delay method for measuring group delay

As the carrier is swept through the DUT, the phase of the modulation varies proportionately to the phase response of the device itself. Group delay is calculated as follows:

$Gd = - \theta e^{\circ} \div (360 * fmod)$

where $\boldsymbol{\theta}_{e^{i}}$ is the phase difference between the demodulated input and output carriers in degrees, and f_{mod} is the modulation frequency. The aperture of this measurement is equal to twice the modulation frequency. The effects of aperture are the same as the phase-derivative method of measuring group delay previously discussed. As the aperture gets larger, the measurement becomes less noisy, but resolution degrades. AM-delay measurements are easily performed with the 8711C series of network analyzers (see Figure 30). A response normalization calibration is performed without the DUT connected. Then the DUT is connected for relative or absolute group-delay measurement. The aperture of this measurement is fixed at 27.8 kHz, with typical accuracy around \pm 10 to 20 ns. The accuracy of this technique is limited by the diode detectors, which have less sensitivity than the narrowband detectors used in the 8653E or 8720D. Group-delay accuracy with the 8753E or 8720D is better, typically around the picosecond range, depending on the smoothing aperture settings. As shown in Figure 30, the phase-derived method (top response) is less noisy than the AM delay method (bottom response). Vector network analyzers use a phase-derived method of group-delay measurement, making it difficult to measure group delay of FTDs with internal inaccessible LOs.



Figure 30. AM delay measurements using the 8711 series

The FM-delay technique is very similar to the AM technique, in that it uses a modulated signal to measure the group delay. The key difference is that FM modulation is used, which eliminates errors due to amplitude noise, and is not sensitive to amplitude limiting in the device under test. However, since the frequency at the output is dependent on the Local Oscillator (LO) frequency, the FM technique has difficulties when the LO is not synthesized.

Time domain

The measurement of group delay through a mixer can be easily accomplished using the time-domain option available on some vector network analyzers. This does not require the extensive hardware and filtering that other traditional methods demand. All that is required to measure absolute group delay and delay linearity is a single mixer, a 50-ohm airline, a short, and a VNA capable of time-domain transformation. (This capability is available on the 8510C and 8720ES analyzers with Option 010.)

The technique uses the measured reflection coefficient from the mixer that is terminated in the 50-ohm airline and the short. The measured frequency response is transformed into the impulse response using the time-domain option on the VNA. Knowing the absolute delay of the airline, the absolute delay through the mixer can be calculated by examining the two-way reflection from the short in time. In addition, the delay linearity of the mixer as a function of frequency can be measured using the gating function on the VNA. The gating function filters the effects of reflections internal to the mixer and isolates only the transmitted signal through the mixer. This gated signal contains the delay distortion introduced by the frequency translation process. In this way, delay linearity through the mixer can now be directly measured in the frequency domain. The impulse response on a VNA simulates a traditional Time Domain Reflectometry (TDR) measurement. TDR is a technique to generate an impulse or step waveform in time that is propagated down a transmission line. The reflections from discontinuities can then be detected in time on the TDR display. The measured time delay represents the two-way electrical distance to the discontinuity in the transmission line. Individual discontinuities can be examined in time if there is adequate electrical separation between them. This same TDR measurement can be simulated using a high-performance VNA capable of time-domain transformation. In this case, the measured frequency response from the VNA is mathematically transformed into the impulse response using the Inverse Fourier Transform algorithm present within the VNA. The measurement accuracy is improved by applying the standard one-port vector error correction to the initial reflection measurement.

As a measurement example, the return loss as a function of frequency of the IF port of a broadband mixer is measured on the VNA. The RF port of the mixer is terminated with an airline and a short. The proper LO drive is also applied to the mixer. The hardware configuration is shown in Figure 31. The VNA is used to measure and transform the frequency response of the mixer under test. The resolution of the time-domain transform on the VNA is directly related to the measured frequency span. In order to examine two closely spaced discontinuities in the time domain, a large frequency span must be measured. However, limitations in the operating frequency range of the device under test also limit the time-domain resolution. It may be necessary to electrically separate the discontinuities whenever possible. Because we wish to isolate the reflected signal from the short in the time domain, placing the short directly on the output of the mixer may not provide enough separation between the mixer's own internal reflections and the signal reflected from the short. Placing a 50-ohm airline between the mixer and the short can improve the measurement resolution by electrically separating the reflections from the mixer and the short, as shown in Figure 31.

For example, the VNA is calibrated for an S11 one-port cal over the IF frequency range of 50 MHz to 10.05 GHz. The LO was set to a fixed value of 20 GHz. This IF range is chosen for two reasons; it is within the operating range of the mixer's IF port, and it allows the VNA to be used in the lowpass mode of operation. The low-pass mode yields the highest resolution in the time domain.

The measured return loss of the IF port as a function of frequency is shown in Figure 32. The return loss shows the typical peaks and valleys that occur when multiple reflections are added and subtracted over the measured frequency range. This frequency response is then transformed into the impulse response using the time-domain option on the VNA. Figure 33 shows the impulse response of the mixer terminated in the airline/short.



Figure 31. Hardware configuration for measuring the absolute delay and delay linearity of frequency translation devices



Figure 32. Measured return loss from the IF port of the mixer as a function of frequency, measurement made using a shorted airline placed on the RF port



Figure 33. Low-pass impulse response of the mixer using a shorted airline placed on the RF port

This figure represents the TDR response as a function of time. It shows the individual reflections from several discontinuities as the impulse waveform propagates along the transmission line. The position of each impulse shows the electrical distance in time from the calibration plane.

The amplitude of each impulse shows the average amount of reflected signal from each discontinuity. The three reflections to the left of the figure are caused by discontinuities present within the mixer: for example, the input and output connectors and transformers.

The largest impulse on the figure is caused by the reflection from the short placed at the end of the airline. This is the discontinuity of interest, as it represents the signal that is transmitted through the mixer under test. A marker is used to measure the position of the short at the end of the airline in time. Because the discontinuities are electrically separated in time, each reflection can be individually examined. The measured marker value of 888 ps represents the two-way reflection from the short as the signal passes through the mixer and the airline. By knowing the electrical delay of the airline, we can subtract it from the measured delay, and then by dividing the result in half, we can obtain the mixer's absolute group delay of 168 ps. Here we are assuming that the delay characteristics of the mixer are the same as the signal passes through the mixer from the IF to RF and RF to IF ports.

Measuring delay linearity

Delay linearity is also an important characteristic of components used in broadband communication systems. In order to measure the delay linearity of the mixer using this technique, a time filter needs to be applied to the frequency domain data. The time filter is used to isolate the reflection from the shorted airline. The reflection from the short contains the delay distortion introduced by the frequency translation process as this signal is passed through the mixer. The time filter or gating function is positioned on the time-domain response around the reflection from the short, as shown in Figure 34. This gated measurement now represents only the response of the signal that passed through the mixer. Eliminated are all other reflections from the masurement: the mixer's own internal reflections and any secondary reflections between the mixer and the short. Only the reflection from the short is allowed to pass through the time filter.

Once the gate is properly centered, the time-domain function can then be turned off while leaving this gate activated. This will yield the frequency response of just the first reflection from the short, the signal that passed through the mixer.







Figure 35. Measured delay linearity of the mixer as a function of frequency

The delay of this gated frequency response is shown in Figure 35. This figure shows the mixer's delay linearity as a function of frequency. In this case, the measured delay linearity is less than 100 ps peak-to-peak over a 10 GHz span, and only 50 ps over the middle 8 GHz.

Note that the ends of the frequency range show a downturn in the measured delay response. This may be the result of the windowing function and data extrapolation that is applied to the measured frequency response data. Generally, this windowing process distorts the measurements at the very ends of the gated frequency response. The initial frequency span should be chosen larger than required to avoid possible errors in the frequency range of interest.

The measurement of group delay through the mixer can also be characterized using the reflection from the RF port of the mixer. In this case, the IF port of the mixer is terminated with the airline and short. Here, the time-domain transformation is typically operated in the band-pass mode, because the frequency range of the mixer's RF port does not typically operate down to DC. The band-pass mode allows arbitrary selection of the start and stop frequencies, which is very useful for band-limited devices. Keep in mind that the band-pass mode will reduce the time-domain resolution when compared to the low-pass mode covering the same frequency range. In this case, a larger frequency range or longer airline may be required in order to improve the time-domain resolution.

Reflection measurements

Now that transmission measurements have been covered, let's move on to reflection measurements. Reflection measurements are linear, even when testing frequency-translating devices since the reflected signal does not undergo a frequency shift. Therefore, these measurements are essentially the same as for filters and amplifiers, with a few minor variations. The systematic errors in the test system and the calibration techniques used to remove them are the same. Narrowband detection should be used if available.

Mixers are three-port devices, and the reflection from any one port depends on the conditions of the other two ports. When measuring reflection on a three-port device, it is important to terminate the ports that are not being tested with an impedance typical of actual operation. While this is often the characteristic impedance Z_0 (usually 50 or 75 ohms), it could also be more complex. For example, if the IF port of the mixer is directly connected to a filter, then this filter should be used when testing RF- or LO-port reflection. In this case, it might be necessary to disconnect the IF port of the mixer from the test set and connect the appropriate load impedance.

When testing the RF or IF ports, the LO signal should be applied at the power level that will be used in actual operation. Since the bias point and impedance of the mixer diodes is a function of LO level, the reflection at the other ports will also be a function of LO level.

Figure 36 shows the different RF-port VSWR results obtained with a broadband load versus the IF termination that would be used in actual operation. The lower trace was measured with the IF port terminated with a 50-ohm load. The upper trace was terminated with a filter and then a 50-ohm load. The effect of mismatch between the mixer and filter is readily apparent. Usually, the mismatch is even worse in the stopband where the match is typically quite poor. This is a very common operating environment for a mixer.



Figure 36. Reflection measurement

This measurement was done using narrowband detection. The measurements of VSWR on the LO and IF ports are very similar. For IF-port SWR, the RF port should be terminated with a matched impedance and the LO signal should be applied at its normal operating level. For the LO port VSWR, the RF and IF ports should both be terminated in conditions similar to what will be present during normal operation.

Testing converters and tuners is easier since there are only two ports and the LO will already be at the correct power level. The same requirement for proper termination of either the RF or IF port also applies.

Isolation measurements

Isolation is a measure of the leakage or feedthrough from one port to another. The more isolation a mixer provides, the lower the feedthrough will be. Isolation is a transmission measurement since the stimulus is applied at one port and the response is measured at another. However, you are measuring a signal at the same frequency as the stimulus, and not the frequency-shifted signal. As is the case for reflection measurements, isolation/ feedthrough is dependent also on the termination and LO power on the ports not being tested.

Three main isolation terms are of interest: RF-to-IF feedthrough, LO-to-IF feedthrough and LO-to-RF feedthrough. The latter term is often important if the mixer is near the front end of the receiver or tuner that is connected to a cable or antenna. In this case, significant LO leakage could cause interference in other frequency bands. Both the LO isolation terms are small for single- and double-balanced mixers. RF to IF feedthrough is also low in double-balanced mixers. RF feedthrough is usually less of a problem than LO to IF feedthrough because in most cases, the LO power level is significantly higher than the RF power level.

RF-to-IF feedthrough is measured with the same instruments and setup used to measure conversion loss (except frequency-offset mode is not needed when using a vector network analyzer such as the 8753E). Because the source and receiver frequencies are the same, set up the network analyzer to use narrowband detection to make the measurement. The only difference in the hardware configuration is that the IF filter needs to be removed so the RF feedthrough will not be filtered out. Depending on which network analyzer is used, a frequency-response calibration or full two-port calibration should be performed to improve measurement accuracy.

The RF feedthrough level is very dependent on the level of applied LO signal. For this reason, the measurement should be made with the LO signal present at its normal operating level.

LO feedthrough measurements are made the same way as described above. Remember to terminate the unused port with the proper impedance. The LO power level used for the measurement should be the same level that is used during actual operation of the mixer.



Figure 37. Isolation



Figure 38. RF feedthrough

Feedthrough measurement of converters and tuners

The procedure for measuring RF to IF feedthrough of a converter or tuner is identical to that of a mixer. Since an IF filter is often included in the DUT, the RF leakage is often very small. An instrument with high dynamic range should be used to make the measurement, and averaging and a reduced IF bandwidth can also be required to lower the noise floor of the test instrument.

Measuring LO leakage requires a different technique and test instrument. Since the LO port is typically inaccessible, a normal network analyzer transmission measurement cannot be used. The best instrument to use for measuring LO leakage is a spectrum analyzer. The spectrum analyzer is connected to either the RF or IF port and tuned to the frequency of the LO signal. Again, the unused port should be terminated. If the LO frequency of the converter or tuner is known to within a few kHz, then the tuned-receiver mode of a network analyzer such as the 8753E could also be used to make this measurement.

The plot in Figure 39 shows a fairly high level of LO leakage present at the output of a converter. If a scalar network analyzer were used to measure conversion gain of this device, this signal could cause some measurement inaccuracy because the analyzer's broadband detectors would include the LO as well as the desired IF signal. In cases such as this, additional filtering should be added to reduce the level of the LO feedthrough when measuring conversion gain.



Figure 39. Feedthrough of tuners and converters

Up to this point, the possible test equipment configurations have been discussed, along with linear measurements such as conversion loss, relative phase, reflection, and isolation. One configuration that has not been thoroughly discussed is the upconversion/ downconversion configuration. This configuration is useful for testing FTDs on vector network analyzers that do not have a frequency offset feature. Furthermore, this configuration along with external error-term manipulation via an external computer can yield very accurate, lower ripple magnitude and phase measurements.

Absolute Group Delay – A More Accurate, Lower Ripple Technique

Although this section describes a more accurate technique for measuring absolute group delay, the same principles can be applied for making more accurate, lower ripple phase-related measurements such as relative phase and relative group delay, as well as very accurate conversion loss measurements.

This section provides more advanced techniques and the operator should understand network-analyzer data transfer and error-term manipulation. An external computer is required. Unlike the previous technique for absolute group delay in which a response-only calibration is performed through a characterized mixer, the accuracy of this technique depends upon using additional vector error correction to calibrate the test system in order to reduce measurement uncertainty created by mismatch interaction between the mixer and the system.

This technique uses an upconversion/downconversion configuration. It consists of a twostep process.

First, a calibration standard needs to be characterized for both magnitude and delay or phase. The standard's delay response can be characterized using any one of the techniques described in the earlier section on group delay. As an example, a mixer pair is measured in which one mixer is used as an upconverter and the other as a downconverter. Use the simple approximation that a single mixer's group delay response is just half the measured response of the pair. The magnitude response of the cal standard can be determined using either the frequency offset mode on a vector network analyzer such as the 8753E or a scalar network analyzer such as the 8757D. Once the magnitude and delay response is determined, a common data file can be created using the measured responses as a function of frequency. This data file will be used to modify the error terms within a vector network analyzer during system calibration.

Second, the calibration mixer is used to calibrate the test system. During the calibration, the calibration mixer is the through standard. Since the calibration mixer has been characterized, its effects can be removed.

The effects of the through standard are removed by modifying error terms extracted from the network analyzer. The calibration mixer data file along with additional measured data are used to modify the extracted error terms. These modified error terms are imported back into the network analyzer where the calibration calculations are performed. Therefore, the network analyzer perform all the error-correction calculations, but the external computer is used to modify the error terms.

Three different levels of error correction can be achieved depending on how much accuracy is needed. The first level removes frequency-response error, the second level removes frequency response and source mismatch, and the third level removes frequency response, source mismatch, and receiver mismatch. The complexity and the steps required increase for each level of accuracy. Each will be discussed in detail.

Let's begin with a discussion of the first step.

Measurement configuration using the mixer pair

When making FTD measurements the measurement accuracy of a network analyzer depends upon knowing and correcting for the reference (or calibration mixer standard) used during the calibration process. The calibration mixer, along with additional standards, is used to correct for errors within the test system. The calibration mixer also provides the necessary frequency translation required during the system calibration process. In order to use the calibration mixer as a standard, it must have certain attributes. See *Appendix A* for a list of attributes required in the calibration mixer.

The typical measurement configuration consists of two mixers, placed as an upconverter and a downconverter in order to obtain the same RF input and output frequency at the network analyzer's test ports. The vector network analyzer supplies and measures these RF signals. A common LO source is shared between the two mixers. A typical configuration of a two-mixer pair including accessory hardware is shown in Figure 40.



Figure 40. Typical configuration for a mixer pair

Two key components are required when making upconverter and downconverter measurements on the mixer pair:

IF filter: The function of the IF filter is to separate the desired mixing product from the undesired product.

Attenuators: These are used to insure well-matched ports on the individual mixers and the test system. Using sufficient attenuation and vector error correction during the characterization process will reduce these effects. You should avoid excessive attenuation since this may introduce unwanted noise in the measurement system, and possibly making the errors larger than a system with less attenuation. The value of attenuation to use will depend on your selected configuration. To review the effects of attenuation, please refer back to the *Magnitude Measurements* section.

Another approach would be to replace the attenuation with an isolator between the first mixer and the filter. The isolator may be a good way to reduce mismatch effects while maintaining a low insertion-loss measurement path in the system.

Labeling conventions

The RF port of the mixer A (mixer A will later be used as the calibration standard for the test system) will be referred to as A_{RF} , the IF port as A_{IF} , and the LO port as A_{LO} . The S-parameter for a reflected signal from the RF port will be referred to as S11A_{RF}. All measured S-parameters will consist of a complex number with real and imaginary values. The measurement group delay of mixer A will be represented by GD_A. The group delay is specified by GD_A with a value in the units of time. To simplify the equations throughout this paper, refer to Figure 41 as a guide.



Figure 41. Mixer labeling example

The test configuration is as shown in Figure 42. A good measurement practice is to maintain short distances between the components and the VNA. Shorter cable lengths result in less ripple effects. If possible, connect test components directly to port 2 of the VNA and use a cable from port 1 to complete the measurement path. This configuration will minimize ripple in S21 measurements caused by the receiver mismatch of the analyzer. Mixer B, the two filters and any additional cables and attenuators connected to port 2 of the network analyzer will be referred to as the hardware interface chassis (see Figure 42). The function of this chassis is to provide the necessary frequency conversion and filtering for the vector network analyzer.



Figure 42. Diagram of the suggested component connections

Proper filtering

Depending on the level of filtering and isolation provided in the measurement system, some spurious signals may be measured by the network analyzer. It is beneficial to make an initial test on the VNA to verify the effects of "spurs" in the measured response. Performing measurements on a pair of mixers over the required RF and LO frequency ranges is required to determine if additional filtering or isolation is needed in the test system. This measurement will also be used to optimize the VNA settings for IF BW and IF averaging.

LO effects

If measurements at different LO frequencies are required, it is necessary to verify the frequency response of the system components across the LO range. It is assumed that the LO drive level to the mixer is constant as the LO frequency is stepped through its specified range. If the LO drive level changes sufficiently to alter the mixer performance then adjustments may be required to minimize these effects. Mismatch effects may also change the LO drive to the mixers, therefore the addition of padding and isolation might be required to maintain the accuracy of the measurements. The LO source might also require amplification to sufficiently drive the two mixers.

System calibration and test

During the calibration and measurement, it is best to use the step sweep mode. This sweep mode verifies phase lock at each measurement point and yields the highest accuracy. If the mixer pair is a noninsertable device, you should calibrate the VNA with the "swap-equal-adapter" technique. Please refer to the VNA user guide for the details of this calibration procedure.

Calibrating the test system with the calibration mixer

Calibration configuration

When making accurate measurements using a VNA, the analyzer must be calibrated to remove the effects of systematic errors from within the test system. In a traditional VNA calibration, a series of known standards is connected at the test ports and measured. Next, a set of error terms is created, based on the measurements and knowledge about the standards. These error terms are used to correct subsequent measurements from errors contained within the test system.

To provide absolute measurements of frequency-translation devices, such as mixers, upconverters and downconverters, it becomes necessary to calibrate the system with a known device that can also translate the frequency during the test system calibration. A calibration mixer is used to calibrate the system after its absolute S-parameters are known. The systematic error terms can then be calculated based on measurements and knowledge of the standard(s)used.

A typical configuration of the test system, which includes the hardware interface chassis, is shown in Figure 43. The calibration mixer standard is connected between test ports 1 and 2 during the calibration stage. Once the system has been calibrated, and the effects of the calibration mixer accounted for, a DUT can be inserted between the test ports and measured. As a calibration verification check, the calibration mixer can be measured and compared to its stored data files. If the two sets of data files are not equal, then the test system has not been properly calibrated.



Figure 43. Typical configuration of test system

All connections to the test system will be performed at the system test port 1 and 2 as labeled in Figure 43. A coaxial calibration kit will be required when performing reflection calibrations of the test system at the system test port 1. If the calibration mixer or DUT is "noninsertable" into the test system at the system test ports 1 and 2, then use the swap-equal-adapter technique to compensate for the test-port connections.

As previously described in Figure 43, the hardware interface chassis contains the necessary filtering, padding and frequency translation required by the test system and the DUT. The chassis is to remain connected to the VNA for the duration of the calibration and test unless otherwise stated. If the chassis is disconnected from the test system after calibration is performed, a new calibration would then be required. Notice that since the chassis contains a mixer for frequency translation, this mixer requires a phaselocked LO, similar to the one provided to the calibration mixer or DUT. A signal generator set to the appropriate LO frequency can provide theses two signals. If the DUT contains an internal LO, then a portion of this signal can be supplied to the calibration mixer and hardware interface chassis during the calibration and test.

Calibration error terms and equations

This section introduces you to the pertinent calibration error terms and equations. It serves as an overview. A more detailed, step-by-step procedure for the calibration is presented in the succeeding *Test Procedure for Calibrating the Test System* section.

The first step to measuring a DUT requires calibrating the test system using the calibration mixer standard. During the calibration, the S21 of the calibration mixer and test system is measured as a two-port device that yields (Equation A).

$$S21^{M}_{MXR} = \left[\frac{1}{1 - S11^{A}_{MXR} \times E_{sF}}\right] \times S21^{A}_{MXR} \times \left[\frac{1}{1 - S22^{A}_{MXR} \times E_{LF}}\right] \times E_{TF} \qquad Equation A$$

As shown, the measured transmission response, $S21^{A}_{MXR}$, is a function of the actual S21 of the mixer, $S21^{A}_{MXR}$, and several error terms. Only the errors terms associated with the forward direction are considered because the hardware interface chassis prevents measurements to be made in the reverse direction, at the system test port 2.

 $S21^{A}_{MXR}$ is the actual S21of the mixer. This information should be contained in the stored data files of the calibration mixer or it can be measured.

E_{SF}, is the source match at the system test port 1. It can be determined using a standard one-port calibration techniques over the RF frequency range.

 E_{LF} is the input match of the hardware interface chassis, $S11^{A}_{CH}$, which is the system test port 2. The $S11^{A}_{CH}$ can be measured directly by measuring the return loss of this port after the VNA is calibrated over the IF range using a standard one-port coaxial calibration.

 $S11^{A}_{MXR}$ and $S22^{A}_{MXR}$ are the actual input and output match of the calibration mixer. These are measured at the RF and IF frequencies, respectively. This information should be contained in the stored data files of the calibration mixer or it can be measured.

Using the test system's error terms and the characterization data file of the calibration mixer, the forward tracking error term, $E_{\tau F}$, of the system can be obtained (Equation B).

$$E_{TF} = \frac{S21_{MXR}^{A}}{S21_{MXR}^{A}} \times \left[1 - S11_{MXR}^{A} \times E_{SF}\right] \times \left[1 - S22_{MXR}^{A} \times E_{LF}\right]$$
Equation B

This adjusted error term, $E_{\tau F}$, is later substituted into the VNA when making measurements on the DUT. As shown above, $E_{\tau F}$ can be corrected for the forward mismatch effects in the system. If the source and receiver mismatch is low then it may not be necessary to correct for all the error terms in the equation. This will be covered in the test-system calibration section to follow.

Now that all the forward error terms of the test system have been obtained, a measurement on a DUT can be performed. The DUT measurement can be represented with a similar equation replacing the calibration mixer terms with the DUT (Equation C).

$$S21_{DUT}^{M} = \left[\frac{1}{1 - S11_{DUT}^{A} \times E_{SF}}\right] \times S21_{DUT}^{A} \times \left[\frac{1}{1 - S22_{DUT}^{A} \times E_{LF}}\right] \times E_{TF} \qquad Equation C$$

The actual match of the DUT, namely S11^A_{DUT} and S22^A_{DUT} can be measured using a standard one-port calibration over the RF and IF frequency ranges, respectively. Using the error terms and the measured value of the DUT, the actual S21 of the DUT can be calculated. As mentioned previously, not all error terms may be required to obtain an accurate estimate of the actual DUT response, S21^A_{DUT}. If the mismatch of the test system or the DUT is low, then correcting for the source and load match effects may not be necessary. Equation D shows the calculation for S21^A_{DUT} including source and load match correction.

$$S21_{DUT}^{A} = \frac{S21_{DUT}^{M}}{E_{TF}} \times \left[1 - S11_{DUT}^{A} \times E_{SF}\right] \times \left[1 - S22_{DUT}^{A} \times E_{LF}\right]$$
Equation D

It is now time to apply these equations to a system calibration and test. The following procedures outline the steps used to calibrate the system using various levels of error correction. Each level of error correction increases the accuracy in the system calibration by including additional error terms as described above. Once the system is calibrated, the same techniques can be applied to measurements of the DUT.

Test procedures for calibrating the test system

The basic concept for calibration of the test system involves inserting the calibration mixer into the measurement path between the system test ports 1 and 2. A measurement sweep is performed and the calibration error terms are calculated based on the measurement. Because the calibration mixer is removed from the system when performing a measurement on a DUT, the system's calibration error terms are adjusted to remove the contribution from the calibration mixer. The calibration mixer essentially provides the frequency translation required during the calibration of the test system.

There are several levels of error correction that can be performed when calibrating the test system. The simplest error correction technique characterizes the frequency response of the test system and does not include any source and receiver match effects. Here, the calibration mixer is placed in the test system and a S21 measurement is performed on the system and mixer. The measurement is adjusted for the calibration mixer response and the remaining response term is used to normalize subsequent measurements of the DUT. This technique does not remove the mismatch effects between the source and input to the calibration mixer. It also does not remove the effects of the receiver match interaction with the calibration mixer. Depending on the level of mismatch within the system and calibration mixer, an error or ripple in the calibration term might be introduced. This ripple will present itself when performing measurements on the DUT. If the ripple is too large, then additional error correction might be required.

Other levels of error correction include characterizing the source and receiver match effects during the calibration procedure. Taking these mismatches into account during calibration reduces the level of ripple in the DUT measurements. Additional calibration standards and measurements are required in order to characterize and remove these mismatch effects. To accurately characterize the effects from both the source and receiver mismatch, measurements are required at the RF and IF frequency ranges, respectively.

Below are the calibration procedures for the three types of error correction that can be implemented. Each procedure corrects an additional error term to increase the accuracy of the calibration.

First-order error correction during system calibration: Frequency-response term only For the first level of error correction, the forward tracking term, E_{TF} , for the system components (network analyzer, hardware interface chassis, cables, etc.) is measured directly. This error term is adjusted for the calibration mixer response and used to normalize DUT measurements. For this level of error correction, there is no adjustment for source and receiver mismatch effects that might introduce ripple in the error term, E_{TF} .

- Place the calibration mixer into the test system at the system test ports 1 and 2 and supply the appropriate LO to the mixer and the hardware chassis. Perform a response through calibration and extract the error term, $E_{\tau F}$, from the VNA. Using the data supplied with the calibration mixer, recall the group delay and the magnitude of S21. The S21 phase of the calibration mixer will be reconstructed from the group delay using the expression below.

 θ = mod 360 (-Gd x 360° x Frequency)

- Adjust E_{TF} by removing the contribution of the calibration mixer from the system's through response. Note: a different E_{TF} term should be created for each setting in LO frequency using the appropriate calibration mixer data from the file. Based on the calibration mixer's response, a new forward tracking error term, E_{TF}^{NEW} , will be created using the following calculation.

$$E_{TF}^{NEW} = \frac{E_{TF}}{S21_{MXR}^{A}}$$

 Insert the new tracking term in the VNA's memory. The response calibration has now been adjusted for the calibration mixer. In other words, the actual S21 of the calibration mixer has been removed from the forward tracking error term. The VNA will now apply the appropriate error correction to subsequent measurements.

The system is now ready to measure the frequency translation device under test. The absolute group delay and conversion loss will be displayed on the VNA. As mentioned, this level of error correction does not include mismatch effects from the source and receiver. As a system verification, the calibration mixer should now be measured. Compare the measured group delay and the magnitude of S21 to the data stored on the mixer's data file. The two data sets should be equivalent. Note any ripple in the displayed response. At this point, you should determine if any additional error correction is required to reduce the ripple in the frequency response.

Second-order error correction during system calibration: response and input match

terms In addition to the forward tracking error term used in the calibration, correction of errors resulting from mismatch between the source and calibration mixer RF input can be made. These mismatch effects can introduce error and ripple in the forward-transmission tracking term $E_{\tau r}$, which is used to normalize measurements of the DUT. By characterizing the source match of the VNA and measuring the input match of the calibration mixer, the forward-tracking term can be adjusted for input mismatch errors. A one-port calibration is used to characterize the input match terms and a response through calibration is used for the forward-tracking term. The one-port calibration yields three more error terms: the forward directivity, E_{DF} the source match, E_{SF} , and the reflection tracking, E_{RF} . A substitute full two-port calibration will be created but only the above errors terms will be used by the analyzer, the other error terms will be eliminated from the error correction. This allows the VNA to provide the required error correction calculations. The VNA will also display the corrected S11 and S21 of the device tested. Following the procedure below will create all the necessary calibration information required for a second level of error correction.

- Create a full two-port calibration place holder, which will be used to store the modified error terms. This can be accomplished by using a pre-existing full two-port calibration, which covers the desired RF frequency range. If one does not exist, you can emulate a full two-port calibration over the desired RF frequency range. To emulate a calibration, press all analyzer's keys as if you were performing a calibration, but do not connect any calibration standards. Save this instrument state to memory or disk file.
- The calibration configuration is shown in Figure 38. Perform a S11 one-port calibration over the specified RF range at the system test-port 1. Extract the three error terms E_{DF} , E_{SF} , E_{RF} associated with the system test-port 1 using a computer and HP VEE or similar software.
- Using the one-port calibration, measure the input match, S11^A_{MXR}, to the calibration mixer's RF port. This measurement might have been previously stored during the mixer characterization.

- Perform a response through calibration with the calibration mixer connected between test port 1 and 2. Extract the transmission tracking term, $E_{\tau e}$, from the VNA.
- Using the data file associated with the calibration mixer, adjust E_{TF} by the calibration mixer's S21 and input match using the following equation.

$$E_{TF}^{NEW} = \frac{E_{TF}}{S21_{MXR}^{A}} \left[1 - S11_{MXR}^{A} \times E_{SF} \right]$$

 Recall the previously saved two-port calibration. Insert the following terms into the error terms of this two-port calibration.

The VNA can now be used to measure a DUT. The absolute group delay and conversion loss will be displayed on the VNA. Using this error correction, it is also possible to display the corrected input match, $S11^{A}_{DUT}$, of the DUT. The error correction implemented adjusts for systematic errors in the frequency response and source match of the system. If different LO settings are required, it might be necessary to create a new set of error terms for each LO frequency used. The calibration mixer should be measured to verify system performance. Compare the group delay and the magnitude of S21 to the data stored on the mixer's data file. The two data sets should be equivalent. Note any ripple in the displayed response. You will need to determine if additional error correction is required to further reduce the ripple in the frequency response.

Third-order error correction during system calibration: response, input and output

match terms This level of error correction includes all the above terms for transmission frequency response and source match, but also adds an additional correction for the receiver match of the test system. Only error terms in the forward direction can be implemented in this system. The resulting error terms consist of the frequency response term, E_{TF} , the system test-port 1 terms, E_{DF} , E_{SF} , E_{RF} and the system test-port 2 receiver match term, S11^A_{CH}. The input and output match and the transmission characteristics of the calibration mixer are also required to implement this error correction scheme during system calibration.

- Create a full two-port calibration place holder, which will be used to store the modified error terms. This can be accomplished by using a pre-existing full two-port calibration that covers the desired RF frequency range. If one does not exist, you can emulate a full two-port calibration over the desired RF frequency range. To emulate a calibration, press all the analyzer's keys as if you were performing a calibration, but do not connect any calibration standards. Save this instrument state to memory or disk file.
- The calibration configuration is shown in Figure 38. Perform a S11 one-port calibration over the specified IF range at test port 1. Measure both the match of the hardware interface chassis, S11^A_{CH}, and the IF port of the calibration mixer, S22^A_{MXR}. The calibration mixer data file may contain the IF port match of the mixer. Save this data to the computer.
- Perform a S11 one-port calibration over the specified RF range at the system testport 1. Extract the three error terms E_{DF} , E_{SF} and E_{RF} associated with the system test-port 1 using a computer and HP VEE or similar software.
- Using this one-port calibration, measure the input match, S11^A_{MXR}, to the calibration mixer's RF port. This measurement may have been previously stored during the mixer characterization.

– Perform a response through calibration over the RF range with the calibration mixer connected to the test ports. Extract the forward-tracking term, E_{TF} , from the VNA. Remove the contribution of the calibration mixer from the calibration by adjusting the forward-tracking term by the mixer's transmission response. Also include the effect of the input and output match terms at the RF and IF frequencies, respectively, using Equation A.

$$E_{TF}^{NEW} = \frac{E_{TF}}{S21_{MXR}^{A}} \times \left[1 - S11_{MXR}^{A} \times E_{SF}\right] \times \left[1 - S22_{MXR}^{A} \times S11_{CH}^{A}\right]$$
Equation A

 Recall the full two-port calibration and insert the following error terms into the VNA's two-port calibration memory. Verify that the correct is used for each setting in LO frequency (see Equation B).

$$E_{DF}, E_{SF}, E_{RF}, E_{TF}^{NEW} Equation B$$

$$E_{LF} = 0$$

The VNA can now be used to measure the DUT. The absolute group delay and conversion loss will be displayed on the VNA. It is also possible to display the corrected input match, $S11^{A}_{DUT}$, of the DUT. The error correction implemented adjusts for systematic errors in S21 caused by the frequency response and source match of the system during both calibration and test. It only corrects for receiver match effects during system calibration. To include the effects of receiver match during measurement of the DUT, the output match of the DUT would be required. Using a one-port calibration over the IF range, the output match of the DUT, $S22^{A}_{DUT}$, can be measured. Additional calculations are required to remove the receiver match effects from the actual S21 of the DUT. The match of the hardware interface chassis, $S11^{A}_{CH}$, is also required over the specified IF range.

The corrected S21 for the DUT including receiver match effects as shown in Equation C.

$$S21_{DUT}^{NEW} = S21_{DUT}^{A} \times \left[1 - S22_{DUT}^{A} \times S11_{CH}^{A}\right]$$

Equation C

This calculation can easily be performed on the computer. It is possible to change the error terms of the full two-port calibration to include this effect thereby using the VNA to perform all error correction in the instrument.

As previously discussed, the system calibration can be verified by leaving the calibration mixer connected to the test system and performing S11 and S21 measurements. After calibration is performed, the calibration mixer characteristics are removed from the calibration terms. At this point the calibration mixer can be measured as a DUT. These measured S-parameters can then be compared to those values saved in the characterization data file provided with the calibration mixer. If the system was calibrated correctly then the two sets of data should be equivalent. Depending on the level of error correction used during system calibration and test, there can be slight differences between the measured results and the stored characteristics.

At any time, if the system calibration needs to be re-verified, then placing the calibration mixer into the test system and measuring its S-parameters will provide the system check. As long as the calibration mixer's measured response agrees with the stored characteristics, then the system calibration is still valid.

Appendix A

Calibration mixer attributes

The quality of the response measurement of the calibration mixer depends somewhat on the characteristics of that mixer. These attributes are listed below:

Frequency response The calibration mixer should have a broad frequency response for all the ports, RF, IF and LO ports. A low conversion loss for the mixer is not important in this mixer. It is especially important that there are no sharp discontinuities or variations in the frequency response of the calibration mixer, as these can indicate poor match conditions which can cause errors during calibrations. Finally, the mixer response should not be sensitive to the LO level, provided a sufficient LO level is present. A low level mixer may be desirable for calibration so that the LO may be padded with a well matched attenuator.

Return loss The calibration mixer must have good match (low return loss) on the RF and IF ports. The mismatch between these ports and the test instrument is a major source of error in the measurement of the mixer response. In most cases, the mixer should have well matched attenuator pads added to the RF and IF ports as a composite component with good match. The return loss of the LO port is not so important, but if the LO drive is not well matched, the mismatch can cause variations in the LO drive level, which may affect the mixer response.

Isolation The calibration mixer should have good isolation, so that the RF to IF and LO to IF leakage does not cause "spurs" in the response of the system after calibration. Using proper filtering in the test system will reduce the effect of spurs in the final test system, but the leakage signals may reflect back into the mixer from the LO and output ports of the mixer, re-mix, and cause ripple in the calibration response.

Spurious generation The spurious generation of the calibration mixer should be low. Driving the mixer at relatively low levels, and padding the input of the mixer, will help reduce spurious generation. Spurs created in the mixer may cause spurs in the network analyzer measurement response of the calibration mixer.

Size Surprisingly, the calibration mixer should typically be of a small physical size. A small mixer will have less phase response (and thus lower group delay) so any ripples caused by phase change through the mixer will be less and "smoother", with peaks farther apart in frequency. This helps in measurements where small gain or phase peak-to-peak variation over a small frequency aperture must be measured.

In practice, the calibration mixer should have a much broader bandwidth than the mixer under test, thus ensuring that any mismatch or conversion loss rolloff effects will "common mode" out of the calibration measurement. Broad bandwidth usually means smooth frequency response. The mixer should be double balanced, or even triple balanced to reduce leakage and image responses. The mixer should be mated with high quality attenuators that have good return loss over the entire range of LO, IF, RF and the sum products. The final calibration mixer may have these pads "permanently" attached (e.g., using shrink tubing over the connectors) so that the pad becomes part of the calibration mixer. The loss of the calibration mixer is not too important. The input and output pads should be at least 6 dB, provided the measuring instruments have sufficient dynamic range at these low signal levels.

The network analyzer should also have padding on its input and output ports, placed at the ends of the test cables. Keeping the lengths between the test-port pads and the calibration mixer short will reduce the rate at which mismatch ripples appear, thus making narrow band differential gain and phase measurements less prone to errors from mismatch. Finally an RF band pass or high pass filter should be added between the analyzer ports and the mixer under test, or better yet, between the analyzer coupled port at the sampler. This will reduce the number and size of any "spikes" in the mixer response caused by out of band signals which might be sampled into the measurement IF of the network analyzer.

Appendix B



Figure 44. Connection diagram for fixed IF measurements using an external computer

Example program listing for fixed IF measurements using a single external source as an LO

1 !*** Fixed IF Downconverter Measurements using the 8753E Network Analyzer and ESG-D3000A

- Signal Generator ****10 ! RE-SAVE "FIXED_IF"
- 20 PRINTER IS CRT
- 30 DIM Marker(51) !Create an array to hold the conversion loss values
- 40 ASSIGN @Ana TO 708 ! 8753E Vector Network Analyzer Address is 708
- 50 ASSIGN @Lo TO 719 ! ESG-D3000A Signal Generator Address is 719
- 60 OUTPUT @Ana;"Pres" ! Instrument Preset
- 70 OUTPUT @Lo;"*RST" ! Instrument Preset using the SCPI command
- 80 OUTPUT @Ana;"FORM4" ! 8753E Data Transfer Format, ASCII with no header
- 90
- 100 ! Enter Frequencies and Power Levels for Mixer under Test
- 110 !
- 120 INPUT "Enter the Fixed IF Frequency (MHz)", If_freq
- 130 INPUT "Enter the LO Start Frequency (MHZ)",Lo_start_freq
- 140 INPUT "Enter the LO Stop Frequency (MHZ)",Lo_stop_freq
- 150 INPUT "Enter the Number of Frequency Point to Measure(3,11,21,26,51)",Pts ! Can be increased
- 160 IF Pts<>3 AND Pts<>11 AND Pts<>21 AND Pts<>26 AND Pts<>51 THEN GOTO 150
- 170 INPUT "Is the RF>LO (1) or RF<LO (2) ? (1/2)",Rflo
- 180 IF Rflo<1 AND Rflo>2 THEN GOTO 170
- 190 Lo_increment=(Lo_stop_freq-Lo_start_freq)/(Pts-1)
- 200 INPUT "Enter the RF Power to the Mixer Under Test (dBm) (RF>-30 and RF<0)",Rf_power
- 210 IF Rf_power<-30 OR Rf_power>0 THEN GOTO 200 ! maintain adequate level for R-Channel
- 220 INPUT "Enter the LO Power to the Mixer Under Test (dBm)",Lo_power
- 230 !
- 240 PRINT "FIXED IF DOWNCONVERTER MEASUREMENTS"
- 250 PRINT "IF Frequency = ";If_freq;" MHz"
- 260 PRINT "LO Frequency = ";Lo_start_freq;" MHz to ";Lo_stop_freq;" MHz"

270 PRINT "LO Frequency Spacing = ";Lo_increment;" MHz" 280 PRINT "RF Port Power = ";Rf_power;" dBm" 290 PRINT "LO Port Power = ";Lo_power;" dBm" 300 PRINT 310 OUTPUT @Ana;"POIN";Pts ! Set the Number of Points on the 8753E 320 ! *** Set the 8753E RF Attenuator for the proper range *** 330 IF Rf_power<=0 AND Rf_power>=-15 THEN Range\$="00" 340 IF Rf_power<-15 AND Rf_power>=-25 THEN Range\$="01" 350 IF Rf_power<-25 THEN Range\$="02"</p> 360 OUTPUT @Ana;"PWRR PMAN" ! Set Test Port Power Range Setting to Manual 370 Power_range\$="POWR"&Range\$ 380 OUTPUT @Ana;Power_range\$! Set Test Port Power Range 390 OUTPUT @Ana;"POWE ";Rf_power ! Set Test Port Power Value 400 OUTPUT @Lo;"POW ";Lo_power ! Set LO Power on the ESG-D3000 410 !*** Set the 8753E to measure absolute power at the R channel for the Fixed IF 420 OUTPUT @Ana;"MEASR" ! Measure and Display the R channel for absolute power measurements 430 OUTPUT @Ana;"CWFREQ ";If_freq;"MHZ" ! Set the 8753E to CW at the fixed IF Freq. 440 OUTPUT @Ana;"LOFREQ OHZ" ! Temporarily set the 8753E for an LO of 0 HZ 450 OUTPUT @Ana;"FREQOFFS ON" ! Set the 8753E to Freq Offset Mode 460 OUTPUT @Ana;"VIEM ON" ! Show the measured data (CW Freq) on the 8753E 470 ! *** Perform a Receiver Calibration on the 8753E at the Fixed IF Frequency *** 480 INPUT "Disconnect the R-CHANNEL jumper from the front panel, Press ENTER", A\$ 490 INPUT "Connect any IF Components (filter, pad) placed after the mixer to the R-CHANNEL IN, Press ENTER", A\$ 500 INPUT "Connect a cable from the TEST PORT 1 to the IF components on the R-CHANNEL IN, Press ENTER", A\$ 510 OUTPUT @Ana;"REIC";Rf_power ! Set the 8753E Power Level Reference for Rcvr Cal 520 OUTPUT @Ana;"TAKRS" ! Calibrate the 8753E's Receiver for absolute power 530 ! *** Connect the Mixer to the 8753E and set initial Freq Offset conditions 540 INPUT "Insert the Mixer into the setup and Connect the LO, Press ENTER", A\$ 550 OUTPUT @Ana;"LOFREQ ";Lo_start_freq;"MHZ" ! Set LO Freq setting on the 8753E 560 OUTPUT @Lo;"FREQ:CW ";Lo_start_freg;"MHZ" ! Set the LO Freg on the ESG-D3000A 570 OUTPUT @Ana;"LOPOWER ";Lo_power ! Set LO Power setting on the 8753E(reference only) 580 OUTPUT @Lo;"OUTP:STAT ON" ! Turn ON the LO Power on the ESG-D3000A 590 OUTPUT @Ana;"DCONV" ! Set the 8753E to measure a downconverter 600 IF Rflo=1 THEN ! Set the 8753E RF>LO 610 OUTPUT @Ana;"RFGTLO" 620 ELSE 630 OUTPUT @Ana;"RFLTLO" ! Set the 8753E RF>LO 640 END IF 650 OUTPUT @Ana;"MARK1 0" ! Turn Marker ON for reading data, can be placed anywhere for CW meas 660 OUTPUT @Ana;"VIEM OFF" ! Turn on Freq Offset Block Diagram for Connection Verification 670 INPUT "Examine the 8753E/ESG Screens and Verify the Connections, Frequencies & Power for the Measurement", A\$ 680 OUTPUT @Ana;"VIEM ON" ! Turn on the measurement display 690 PRINT 700 PRINT "LO Freq (MHz) LOSS (dB)" 710 FOR I=1 TO Pts 720 Lo_freq=Lo_start_freq+(I-1)*Lo_increment 730 OUTPUT @Ana;"LOFREQ ";Lo_freq;"MHZ" ! Set LO Freq setting on the 8753E 740 OUTPUT @Lo;"FREQ:CW ";Lo_freg;"MHZ" ! Set LO Freg on the ESG-D3000A 750 WAIT 2 ! Wait to allow 8753E to phase lock, this setting should be optimized 760 OUTPUT @Ana;"OUTPMARK" ! Command to output marker value 770 ENTER @Ana;Mark1,Mark2,Stim1 ! Enter the marker value 780 Marker(Pts)=Mark1-Rf_power ! Calculation for conversion loss (dB): Meas-Input levels(dBm) 790 PRINT USING 800;Lo_freq,Marker(Pts) 800 IMAGE 1X,4D.DD,10X,4D.D 810 NEXTI 820 PRINT 830 PRINT "Measurement Complete" 840 END

Example output: mixer is the HP Avantek TFX-18075L

FIXED IF DOWNCONVERTER MEASUREMENTS						
IF Freque	ency	= 170 MHz				
LO Frequ	lency	= 2400 MHz to 2600 MHz				
LO Frequ	lency Spacing	= 20 MHz				
RF Port	Power	= -20 dBm				
LO Port	Power	= 10 dBm				
LO Freq	(MHz) LOSS (d	B)				
2400	-8.5					
2420	-6.9					
2440	-7.6					
2460	-8.5					
2480	-7.3					
2500	-7.2					
2520	-8.5					
2540	-7.1					
2560	-7.5					
2580	-8.6					

Appendix C

-6.9

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Uncertainty in mixer group delay measurements

Limitations of equipment Vector network analyzers (VNA) are commonly used to measure gain, phase and group delay of linear devices. The accuracy of such measurements depends upon the stability of the test equipment, and the quality of the user calibration. For these calibrations to work, the VNA typically depends upon the fact that the input and output frequencies are the same. The user calibration consists of measuring several carefully controlled calibration standards, and using the results of these measurements to remove the effects of the test equipment response from the device measurement. Unfortunately, no such calibration devices exist for mixers. For mixer amplitude response, this does not create a great barrier. Other techniques can be used to measure the power applied to a mixer and the power delivered to a load from a mixer. The use of precision power meters and scalar network analyzers in this application is common. Further, a precision power meter can be used to calibrate a VNA which can make these measurements faster, and with more accuracy. But for phase response and group delay, there exists no reference-standards that may be employed to perform such a calibration.

For the case of group delay, there exists a further level of uncertainty. Since any measurement contains noise, which is roughly constant with frequency, as the frequency spacing gets smaller and smaller the group delay calculation gets increasingly noisy. Wider spacing leads to frequency uncertainty as well as errors if the group delay response is not flat with frequency, as in a filter. We'll see shortly that the aperture definition also limits delay measurement methods that don't depend of phase measurements.

Uncertainties in methodologies Each methodology for measuring group delay, up/ downconversion, modulation delay, and time domain, contains its own uncertainties.

Up/down conversion The major uncertainty in the up/down conversion method is in determining the delay and the amplitude response of the calibration mixer. One early approach to remove the effects of the second mixer in the up/down conversion was to use two small calibration mixers in an up/down conversion configuration, with the total delay being measured. A filter is required between the mixers to remove the effect of the unwanted side band. The total delay consists of the delay of the first mixer and the delay of the second mixer and the delay of the filter, which may be measured independently.

After subtracting off the delay of the filter, the remaining delay is divided by assigned to the each mixer. In this case, the uncertainty is on the order of the delay of the mixer. One can argue that since neither mixer has negative delay (this argument might break down if unwanted side bands are allowed to mix in) then even if one mixer has zero delay, the most delay in the other mixer is the delay measured. Thus, if half the measured delay is assigned to each mixer and the maximum error is also half the measured delay. By using very small mixers, which are available with delays less than 500 ps., this calibration mixer may be used to measure devices with greater delays.

This argument does not assume that the mixers are the same, only that they do not have negative delay. If the mixers are of the same type, one might argue that the delay in each should be approximately the same and thus the error much less than assumed above. One approach would be to measure the mixers in both directions, first forward then reverse. However, even if the result gave the same reading, it does not guarantee that the mixers' delays are the same. At most, it shows that the mixers are similar when both are used as up converters, or both used as down converters.

In fact, a single mixer may behave entirely differently in as an up-converter than as a down converter, but the up/down combination would still look the same. For example, suppose mixer A has 900 ps of delay as an up-converter, and 100 ps as a down converter, and mixer B is similar. Connect A as an up converter and B as a down converter gives 1000 ps of delay. Also, connecting B as an up converter and A as a down converter gives 1000 ps of delay. But clearly, the up conversion delay is not at all close to the down conversion delay.

In controlling the unwanted side bands, the filter used in between the mixers will have mismatch at the unwanted side band (as well as some mismatch of the desired side band) than can cause signals to re-mix and create further errors. In this approach, one can estimate the errors by knowing the match of the converters and the match of the filter. Since only one measurement is being made, only one accounting for these errors is needed.

Modulation delay Uncertainty and errors in the AM delay occur in several main areas. The first is the frequency of modulation and the modulation waveform. If the device being measured does not have flat delay, higher frequencies can cause errors due to modulation side bands being delayed differently. If a square wave modulation is used, the modulation waveform consists of many, discrete modulation frequencies with the higher frequencies being delayed differently than the lower frequencies. A second area of concern is with the detector response to the modulation frequency. If a broad band detector is used, both side bands of the mixer will show up in the detector. If a filter is used, the filter response and mismatch must be accounted for. In most detectors, noise in the detection system limits the lowest frequency to the range of kilohertz to megahertz, thus setting the minimum aperture.

An additional error may occur in the amplitude modulator. If there is delay in the modulating circuitry, this must be accounted for in determining uncertainty. Some systems allow calibrating for this delay by connecting the detector to the source and measuring the delay at the input frequency. However, if the detector is filtered, this is not practical, and if not, it depends upon the demodulation response of the detector to be flat across the input and output range of the mixer.

For AM delay, an additional uncertainty is limiting and AM noise in the measurement. Some devices have built in amplitude limiting, which strips off the amplitude modulation, so cannot be measured using the AM delay technique. Others have noisy LO's which limit the delay sensitivity. In practice, the AM delay technique can give delay measurements in the low nanosecond range. With the advent of small, low delay mixers, the AM technique may not have the resolution of the up/down conversion technique.

Many of the difficulties in the AM technique can be overcome by using FM modulation. It is not sensitive to amplitude limiting in the device under test, and it eliminates errors due to amplitude noise. However, AM to PM conversion can cause some errors. Also, since the frequency at the output is dependent on the Local Oscillator (LO) frequency, if the LO used is not synthesized (as in some low cost converters) the FM technique cannot be used. A further difficulty is that the FM detector is quite complex, and may not have flat broad band response. Also the FM modulation may not have the same delay at all frequencies, particularly if the source used has different filtering for different bands, and the modulation occurs before the filtering, which is typical. The FM technique is primarily used to measure differential delay across a mixer-under-test that might have narrow filtering following. It is usually not practical to measure absolute delay with the FM technique.

Time domain The first uncertainty to consider for the time-domain technique it due to the reflection from the short representing both the upper and lower side band products from the mixer. If the mixer has significantly different delay for the upper (sum) products and the lower (difference) products, then the reflection from the short can be "smeared" and it will not represent the desired delay. This is particularly true when looking at the gated response. In the extreme, there may be two separate responses from the short representing the upper and lower side band individually.

A second source of error occurs if the mixer is not broad band. In this case, the delay through the mixer may be significantly different at different frequencies. The delay measured by the short's reflection is the composite delay of the entire response. However, the gated response should show the differential delay from which the delay at any frequency may be calculated. These measurements are of course subject to the uncertainty mentioned above with respect to the unwanted side band response.

As noted in the chapter on relative phase measurements, the gated time-domain response can produce errors at the edges of the frequency response, mostly due to windowing, gating and re-normalization. In general the first and last 10 percent of the time-gated frequency response is suspect.

Finally, this method depends upon the mixer having identical response as an up- or down-converter, since the signal reflected from the short must be converted twice. Any nonsymmetry in these response will produce uncertainty in the measurement.

Comments on traceability In specifying the performance of VNAs to measure linear devices, manufacturers depend upon traceability to fundamental measurements. This often leads back to such organizations as the National Institute for Standards and Technology (NIST). But there are no such traceable standards for converter delay. Traceability may not be a requirement for an R&D engineer working at his bench, where he understands the block diagram and interactions between components. But in the manufacturing arena, as well as in product acceptance, where there cannot be assumptions made about the internal design of a component, strict limits on the error bounds of measurements are important. The determination of these limits must be based on sound engineering practices and not on wishful thinking about the qualities of devices.

With the introduction of new measurement instruments, such as vector spectrum analyzers and vector signal generators, more techniques are becoming available to lessen the uncertainties in these measurements. Further, new investigations into measurement methodologies may provide the path to generate calibration techniques appropriate to these devices.

Finally, it is clear that new transfer standards are needed in this area. Similar problems have been addressed in other areas such as electro-optic converters, but we are still lacking fundamental standards and techniques for frequency converter measurement traceability.

Appendix D

Related application and product notes

Understanding the Fundamental Principles of Vector Network Analysis, application note 5965-7707E

Exploring the Architectures of Network Analyzers, application note 5965–7708E

Applying Error Correction to Network Analyzer Measurements, application note 5965-7709E

Network Analyzer Measurements: Filter and Amplifier Examples, application note 5965-7710E

In-fixture Microstrip Device Measurements Using TRL*Calibration, application note 5091-1943E

Specifying Calibration Standards for the 8510 Network Analyzer, application note 5956-4352E

Applying the 8510 TRL Calibration for Non-Coaxial Measurements, application note 5091-3645E

Measuring Noninsertable Devices, application note 5956-4373E

Improving throughput in Network Analyzer Applications, application note 5966-3317E

Using a Network Analyzer to Characterize High-Power Components, application note 5966-3319E

Other suggested reading

"Design of an Enhanced Vector Network Analyzer," Frank David et al., Hewlett-Packard Journal, April 1997.

"Calibration for PC Board Fixtures and Probes," Joel Dunsmore, 45th ARFTG Conference Digest, Spring 1995.

"Techniques Optimize Calibration of PCB Fixtures and Probes," Joel Dunsmore, Microwaves & RF, October 1995, pp. 96-108, November 1995, pp. 93-98.

"Improving TRL* Calibrations of Vector Network Analyzers," Don Metzger, Microwave Journal, May 1995, pp. 56-68.

"The Effect of Adapters on Vector Network Analyzer Calibrations," Doug Olney, Microwave Journal, November 1994.

"A Novel Technique for Characterizing the Absolute Group Delay and Delay Linearity of Frequency Translation Devices," Michael Knox, 53rd ARFTG Conference Digest, Spring 1999, pp 50-56.

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