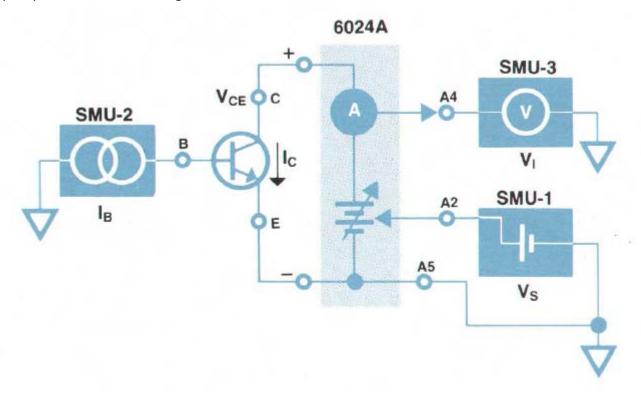
HP 4145A - Power Transistor Measurement Setup

I've been playing around with my repaired HP 4145A, and the setup to measure power transistors, as described in the issue of October 1982 of the Hewlett-Packard Journal (page 10), caught my attention.

The article doesn't provide a lot of detail on how to set up the analyzer. Since I struggled with it for a while, I will describe below how I configured everything to measure a 2N3055 transistor. Someone may find the information helpful.

1) Hardware Setup

I pretty much followed the diagram in the article:



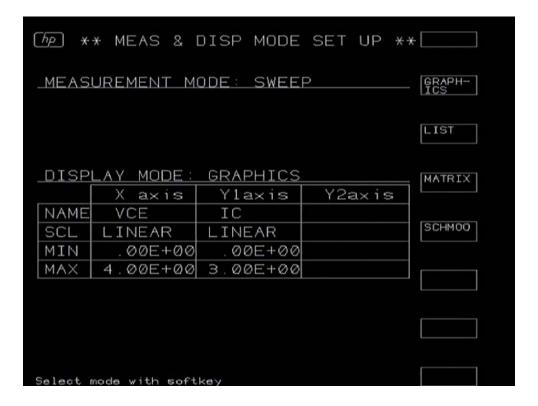
2) Configuration of the 4145A

The following screen captures show how I set up the SMUs and the calculations to convert the sweep voltage into VCE and the output current voltage of the 6024A into IC:

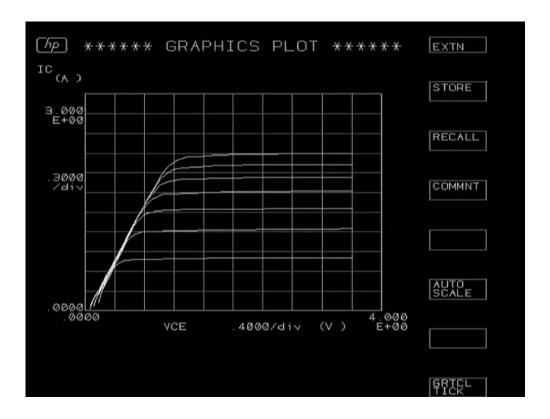
	NAME		SOURCE		LOTTNI I
CHAN	V	I	MODE	FCTN	GENL
SMU1	VS	IS	٧	VAR1	
SMU2	VB	IB	I	VAR2	ACE-IC
SMU3	V	I	I	CONST	[VCE-IC]
SMU4					F-000 000
Vs 1			٧		FET-ID
Vs 2			٧		
Vm 1					PF-JE
Vm 2					VF-IF
USER FCTN 1	NAME (UNIT) :	EXPRESSIO	И		
2	VCE (V)=	ASSIGN			

<i>Б</i> Р ***** S	OURCE SET	UP *****	
	VAR1	VAR2	
NAME	VS	IB	LINEAR
SWEEP MODE	LINEAR	LINEAR	
START	. 0000V	5.000mA	[1.001.0.]
STOP	. 3000V		LOG10
STEP	. 0050V	5.000mA	
NO. OF STEP	61	7	L0G25
COMPLIANCE	40.00mA	10.000V	
CONSTANT	SOURCE	COMPLIANCE	LOG50
I I	.000 A	100.00V	
Select sweep mode	with softkey		

3) Display Mode Setup



This is the result (single measurement, long integration time):



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KI7PFX