

P.C. 09-19194

SUPERSEDES
NONE**-hp- MODEL 3570A NETWORK ANALYZER**

Serial Number: 1331A01615 and Below

SIGN (+/-) ERRORS USING HP-IB

A change has been made on the ASCII output board, A36, that eliminates +/− sign errors which occasionally occurred when using the 3570A on the HP-IB.

When a 3570A is used on the HP-IB, particularly with an -hp- 9825 Calculator, sometimes the 3570A reading will come up positive on the calculator when it was actually negative. This occurs because of a timing problem with the synchronous and asynchronous operations on the A36 board. The timing problem causes a counter that is used to "look up" the ASCII characters to be transmitted over the HP-IB, to omit sending the "sign" character. Thus by default the 3570A output will occasionally be interpreted as positive when it was actually negative.

If you see this problem, a minor change in the clocking technique will eliminate it. Figure 1 shows the previous circuit configuration while Figure 2 shows the change that should be made. The NOR gates on U9 were previously unused. These new connections should be hardwired on the circuit (non-component) side of A36. Do not forget to break the trace that goes directly from U2 to U4. Also note that A36 was changed from 03570-66573 to 03570-66583, however, this modification can be made on either of these boards.

Please note this information in your 3570A Operating and Service Manual, and wherever else applicable.

CEW/kkz/WO

03/80-09

Printed in U.S.A.



FOR MORE INFORMATION, CALL YOUR LOCAL HP SALES OR SERVICE OFFICE or East (201) 265-5000 • Midwest (312) 255-9800 • South (404) 955-1500 • West (213) 970-7500 or (415) 968-9200; OR WRITE, Hewlett-Packard, 1501 Page Mill Road, Palo Alto, California 94304. IN EUROPE, CALL YOUR LOCAL HP SALES OR SERVICE OFFICE OR WRITE, Hewlett-Packard S.A., 7, rue du Bois-du-Lan, P.O. Box CH-1217 MEYRIN 2 -Geneva, Switzerland. IN JAPAN, Yokogawa-Hewlett-Packard Ltd., 9-1, Takakura-cho, Hachioji-shi, Tokyo, Japan 192.

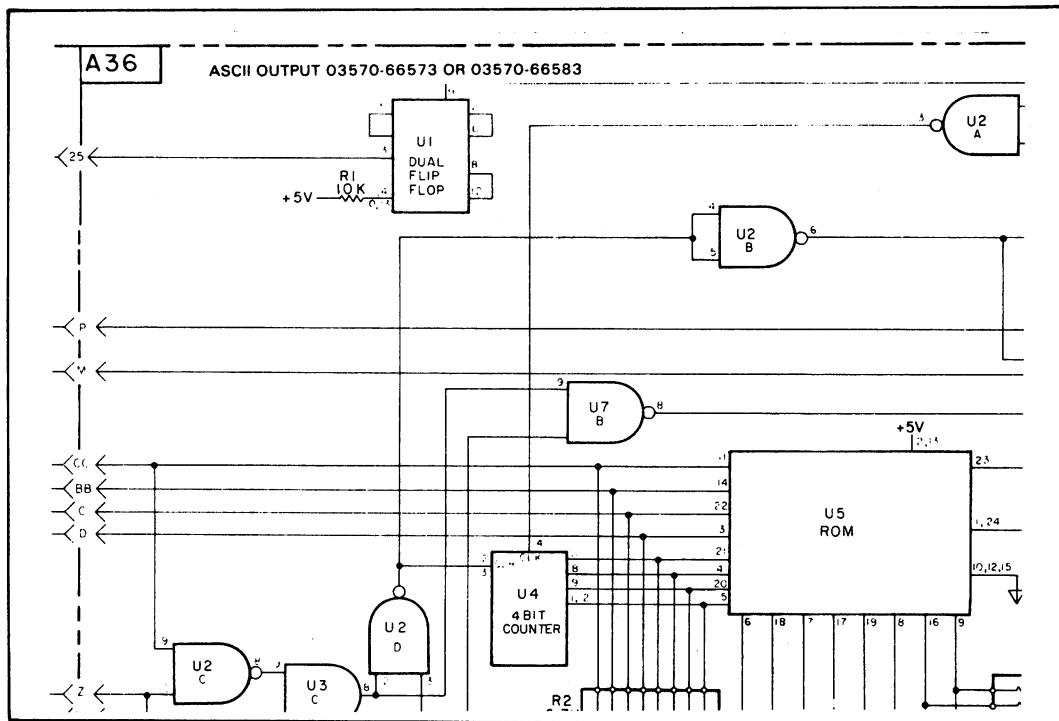


Figure 1. Circuit Before Change.

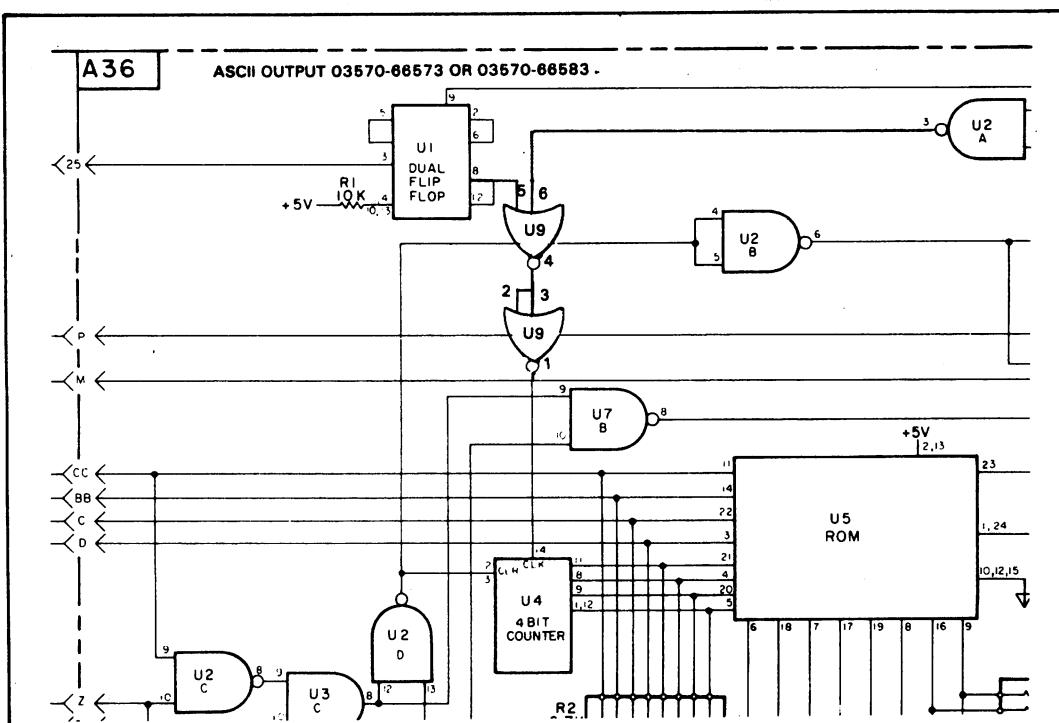


Figure 2. Circuit After Change.