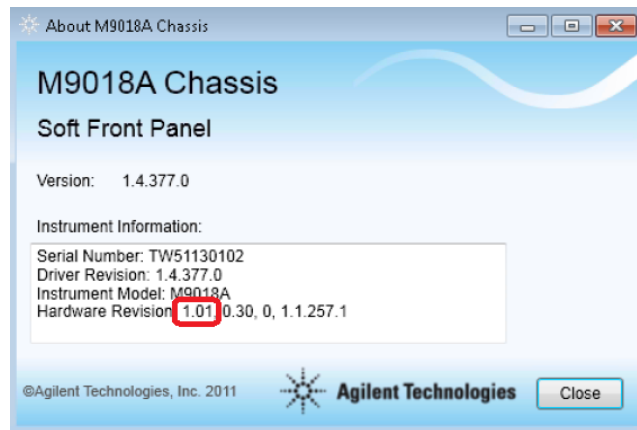# Modification Recommended
Service Note

Supersedes:
M9018A-09

M9018A 18 slot PXIe Chassis

Serial Numbers: TW00000000 through TW54470100

Chassis fail to lock to external 10 MHz reference with Hardware Revision 1.01 FPGA.

ADMINISTRATIVE INFORMATION

ACTION CATEGORY:	X ON SPECIFIED FAILURE [[]] AGREEABLE TIME	STANDARDS LABOR: 0.5 Hours		
LOCATION CATEGORY:	X CUSTOMER INSTALLABLE [[]] ON-SITE (active On-site contract required) X SERVICE CENTER [[]] CHANNEL PARTNERS	**SERVICE INVENTORY:** [[]] RETURN [[]] SCRAP X SEE TEXT	**USED PARTS:**	[[]] RETURN [[]] SCRAP X SEE TEXT
AVAILABILITY: PRODUCT'S SUPPORT LIFE		NO CHARGE AVAILABLE UNTIL: October 2016		
	[[]] Calibration Required X Calibration NOT Required	PRODUCT LINE: BL AUTHOR: TP		

ADDITIONAL INFORMATION:

Parts Required:

P/N   Description   Qty.

NONE

Situation:

It has been found that M9018A with version 1.01 FPGA has an issue which results in failure to correctly lock the backplane 10 MHz reference clock to an external 10 MHz input, or to the 10 MHz reference clock generated from an M9300A frequency reference module. This has an impact on the time alignment performance when synchronizing multiple M9391A Vector Signal Analyzers or M9381A Vector Signal Generators, or any application which relies on the use of the backplane 10 MHz reference for synchronization or frequency locking.

Solution/Action:

1. Go to http://www.keysight.com/find/M9018A.
2. Navigate to Technical Support → Drivers, Firmware & Software → M9018A Chassis Firmware Update Utility.
3. Follow the instructions at the website to update the M9018A FPGA to version 1.07 or later.

Revision History:

Date	Service Note Revision	Author	Reason for Change
10 Sep 2015	01	TP	As Published