MODIFICATION RECOMMENDED

U9401B-01 <u>S E R V I C E N O T E</u>

Supersedes: None

U9401B - i1000D systems

Serial Numbers: All

Current Digital card firmware version 06 does not support i2C/SPI "Read to File" function.

Parts Required: Parts will be order directly from factory. Required parts and tools:

- 1. Lattice download cable
- 2. Lattice programming software tool
- 3. Digital Card firmware Version 07 (H_Code.jed and L_Code.jed)

Contact :

kwan-wee_lee@agilent.com derek-cw_yu@agilent.com

ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:							
MODIFICATION	RECOMMEND	ED					
ACTION [[]] ON SPECIFIED FAILURE CATEGORY: x AGREEABLE TIME	STANDARDS LABOR: 1.0 Hours						
LOCATION [[]] CUSTOMER INSTALLABLE CATEGORY: X ON-SITE (active On-site contract required) [[]] SERVICE CENTER [[]] CHANNEL PARTNER	SERVICE [[]] RETURN INVENTORY: [[]] SCRAP [[]] SEE TEXT	USED [[]] RETURN PARTS: [[]] SCRAP [[]] SEE TEXT					
AVAILABILITY: PRODUCT'S SUPPORT LIFE	NO CHARGE AVAILABLE UNTIL: 4- March - 2016.						
[[]] Calibration Required xCalibration NOT Required	PRODUCT LINE: 80 AUTHOR: CW Yu						
ADDITIONAL INFORMATION:							

Order Parts directly from factory. Contact <u>kwan-wee_lee@agilent.com</u> or derek-cw_yu@agilent.com

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March 5, 2014

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Situation:

Current digital card firmware version 06 cannot support I2C/SPI "Read to file" function. All data will be written as Zero in the bin file thus all of digital cards need to be upgraded to version 07.

Solution/Action:

Follow the below instructions to upgrade Digital Card Firmware to Version 07

Installation of Lattice Programming Software

- 1. Run ispvmSystemV17.8.exe for installation
- 2. Plug download cable to controller USB port



3. Double click ispvmSystem.

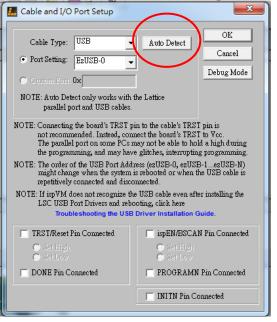


4. Setup IO port and cable. Select Options→Cable and IO Port Setup

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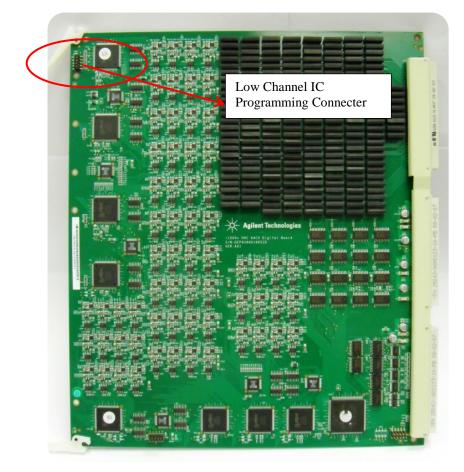
Page 3 of 13

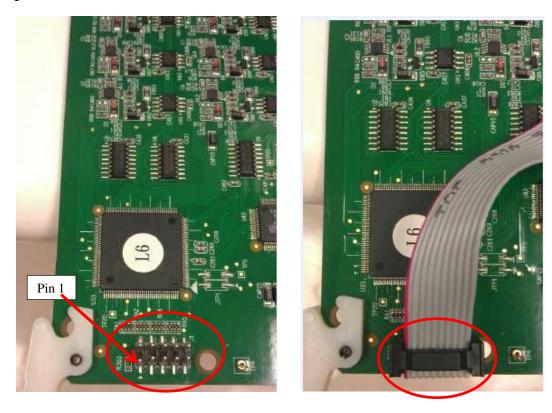
5. Click "Auto Detect" for IO Port Setup and click "OK"



Connection of Download cable to Digital Card Low Channel IC

- 1. Turn off the i1000 system and remove the Digital Card.
- 2. Plug download cable to low channel connecter





- 3. Insert Digital card in card cage with the Download cable attached to it.
- 4. Leave space to allow the Download cable to be placed into the cardcage. If needed, remove the adjacent pin cards.
- 5. Turn on i1000D system power

Program firmware to Low Channel ICs

1. Select isp tools \rightarrow Scan chain

🔝 LSC ispVMR System Versi	on 18.1 - C:\Users\chiaweyu\Desktop\Lattice.xcf	-	81
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	Model 300 Programmer	Ctrl+M	
C:\Users\chiaweyu\Desl	Universal File Writer		
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Add New Device by selecting E	SVE Debugger		
	STAPL Debugger		
	STPL STAPL Processor		
	IME VME Processor		
	Scan Chain	F2	
	Scan <u>M</u> ixed Chain	F3	
	🕂 Board Diagnostics		
	Install/Uninstall LSC USB/Parallel Port Driver		
	BSDL Application Specific BSDL File Generator		
Status	3 <u>Repetitive Download</u>		
	😋 Convert Composite to Jedec File		A
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Page 5 of 13 2. Low Channel IC will be detected and displayed

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1	LFXP6C		FLASH Erase, Progra	N/A	
3 2	LFXP6C		FLASH Erase, Progra	N/A	
3	LFXP6C		FLASH Erase, Progra	N/A	
3 4	LFXP6C		FLASH Erase, Progra	N/A	

3. Load Firmware to program

Index Device List FileName/IR-Length Operation Status 1 LFXPBC FLASH Erase, Progra N/A 2 LFXPBC FLASH Erase, Progra N/A 3 LFXPBC FLASH Erase, Progra N/A 4 LFXPBC FLASH Erase, Progra N/A	0 🖻	Edit View Proje	- 8			
1 LFXP6C FLASH Erase.Progra N/A 2 LFXP6C FLASH Erase.Progra N/A 3 LFXP6C FLASH Erase.Progra N/A 4 LFXP6C FLASH Erase.Progra N/A	\$	🈹 🖦 🍇 🦂	6 B B 🌭 🦘 🦄			
• 2 LFXP6C FLASH Erase, Progra N/A • 3 LFXP6C FLASH Erase, Progra N/A • 4 LFXP6C FLASH Erase, Progra N/A	Index	Device List	FileName/IR-Length	Operation	Status	
	3 >1	LFXP6C		FLASH Erase, Progra	N/A	
● 4 LFXP6C FLASH Erase,Progra N/A	2	LFXP6C		FLASH Erase, Progra	N/A	
	3	LFXP6C		FLASH Erase, Progra	N/A	
Double click	9 4	LFXP6C		FLASH Erase, Progra	N/A	
				Double click		

4. Click Browse and select Firmware file "L_CODE.jed"

Device Information	×					
Part Description:	<u>O</u> K					
Device:	Cancel					
Select LFXP6C	Advanced					
Device Full Name: Package:	Ermand					
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5. Setup Operation options. Select "Flash Erase, Program, Verify, Secure"

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FLASH Erase Only FLASH Verify ID FLASH Display ID	
FLASH Erase Only FLASH Verify ID FLASH Display ID FLASH Display USERCODE	
FLASH Erase Omy FLASH Verify ID FLASH Display ID FLASH Display USERCODE FLASH Read and Save	

6. Repeat Step3 to 5 to complete the setup for the rest of the Low Channel IC.



7.

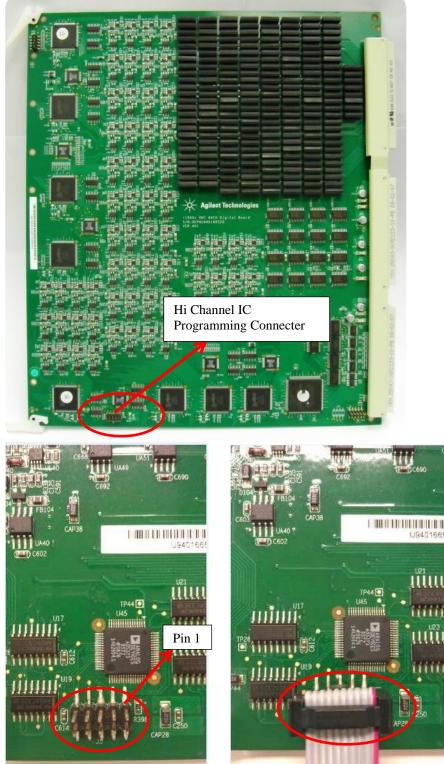
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8 3	LFXP6C	F:\project\DIGITAL\cplt\la\U90331L\dur19.jed	FLASH Erass, Program, Verify	N/A	0x4FFC	
9 4	LFXP6C	F:\project\DIGITAL\cpld\la\090331L\dur19.jed	FLASH Erase, Program, Verify	N/A	Ox4FFC	
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	Operation Done. No	o sutors.				
Device2	LFNP6C: FLASH En	aze,Program,Verify				
	Operation Done. No	o enrora.				
Device3	LFNP5C: FLASH En	ass,Program,Verify				

8. Programming finished, all IC must show Pass, else repeat the process or replace the digital card.

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ا چ	LFXP6C	F:\pmject\DIGI7AL\cplt\lal090331L\dur19 jet	FLASH Erass, Program, Verify	PASS 0x4FFC	
4 2	LFXP6C	F:\project\DIGI7AL\cplt\lal090331L\dur19 jet	FLASH Erass, Program, Verify	PASS 0x4FFC	
🚓 3	LFXP6C	F:\project\DIGITAL/cplt\lal090331L\dur19.jed	FLASH Erass, Program, Verify	PASS 0x4FFC	
4	LFXP6C	F:\project\DIGITAL\cplt\lal090331L\dur19.jed	FLASH Erase, Program, Verify	PASS 0x4FFC	

Connection of Download cable to Digital Card Low Channel IC

- 1. Turn off the i1000 system and remove the Digital Card.
- 2. Unplug download cable from Low Channel connecter and plug into the High Channel connecter



- 3. Insert Digital card in card cage with the Download cable attached to it.
- 4. Leave space to allow the Download cable to be placed into the cardcage. If needed, remove the adjacent pin cards.
- 5. Turn on i1000D system power

Program firmware to High Channel ICs

1. Select isp tools \rightarrow Scan chain

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	Board Diagnostics		
	Install/Uninstall LSC USB/Parallel Port Driver		
	BSDL Application Specific BSDL File Generator		
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2. High Channel IC will be detected and displayed

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2	LFXP6C		FLASH Erase, Progra	N/A	
3	LFXP6C		FLASH Erase, Progra	N/A	
8 4	LFXP6C		FLASH Erase, Progra	N/A	

3. Load Firmware to program

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2	LFXP6C		FLASH Erase, Progra	N/A	
3	LFXP6C		FLASH Erase, Progra	N/A	
3	LFXP6C		FLASH Erase, Progra	N/A	
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Page 10 of 13

4. Click Browse and select Firmware file "H_CODE.jed"

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5. Setup Operation options. Select "Flash Erase, Program, Verify, Secure"

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6. Repeat Step3 to 5 to complete the setup for the rest of the High Channel IC.

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# 4 LFXP6C C:\Agilent_i1000\ATD\OSW_Verify\ FLASH Erase, Progra N/A Image: A state of the state	3 LFXP6C	C:\Agilent_i1000\ATD\OSW_Verify\	FLASH Erase, Progra	N/A	
	4 LFXP6C	C:\Agilent_i1000\ATD\OSW_Verify\	FLASH Erase, Progra	N/A	

7. Click "GO" to start programming

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🐵 3	LFXP6C	F:\project\DIGITAL\cplt\la\090331L\dur19.jed	FLASH Erass, Program, Verify	N/A	0x4FFC	
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	JTAG Chain V	erification. No Errors.	Abort			
Check co	nfiguration setup: Success	ful.				
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	Operation Done. No error	n				
Device2 I	FXP5C: FLASH Erase,Pr	ognun,Verify				
	Operation Done. No error	ra.				
Device3 I	.FXP6C: FLASH Erroy,Pr	ogram,Verify				

8. Programming finished, all IC must show Pass, else repeat the process or replace the digital card.

idex	Device List	FileName/IR-Length	Operation	Status	Checksum/CR.	
1	LFXP6C	F:\purpject\DIGITAL\cpld\lal090331L\dur19 jed	FLASH Erass, Program, Verify	PASS	0x4FFC	
2	LFXP6C	F:\project\DIGLTAL\cplt\lal090331L\dur19 jed	FLASH Erass, Program, Verify	PASS	0x4FFC	
3	LFXP6C	F:\project\DIGITAL\cpld\lak090331L\dur19.jed	FLASH Erase, Program, Verify	PASS	0x4FFC	
4	LFXP6C	F:\project\DIGITAL\cpld\lal090331L\dur19.jed	FLASH Erase, Program, Verify	PASS	0x4FFC	
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Future Tracking Purpose

1. The EDC for all the U9401-66520 at the barcode serial number need to revise to 5409 The original label barcode of the serial number as Figure 1.0, the EDC cover with black permanent marker pen while add additional label 5409.



Figure 1.0

2. On top of the Lattice XP IC label, request to change from H6 & L6 to H7 & L7 as Figure 2.0

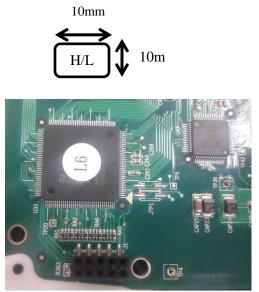


Figure 2.0



Confirm Digital Card Version

1. Run Card Diagnostics in i1000 software

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🙀 File Edit Test Learn Debug View Print Report Window	Diagnostics Setu	p Language Hel	p	
🖆 🖶 🖬 🔝 🙀 🗒 🎒 🎒 🚔	Reset Hardwar	e Ctrl+R	1	
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Serial Number	Slot Informatio			
Prepare SN	Self-Tested Info		tal Times	0:1:8:14 D:H:M:S
Prepare MAC			Pass	807 pcs
Board Name D31			Fail	5 pcs
Fixture ID			Tested	812 pcs
			Pass Yield	99.3842 %
Work Shift 1 Line Number User ID Product Number			Fail Cycle	0 times
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Security Administrator Machine Name				
Fail Log Path Null				
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2. Confirm firmware version 07.

Card Diagnostics	
Message	
Date: 2014/02/25	<u>^</u>
Time: 17:39:16	
Line:	
Machine:	
Factory:	
Department:	
User ID: i1000	
System Type: Press Down	
Control Card Version: 14	E
And Dismosting Detail Descent	-
ard Diagnostics Detail Report	
Active Version Data: 04040404	-
Low Version Data: 07070707	
High Version Data: 07070707	
Slot 22, Digital Pin Card OK	
Active Version Data: 04040404	
Low Version Data: 07070707	
High Version Data: 07070707	
Slot 23, Digital Pin Card OK	
Active Version Data: 04040404	
Low Version Data: 07070707	
High Version Data: 07070707	
Slot 24, Digital Pin Card OK	
Active Version Data: 04040404	
Low Version Data: 07070707	
High Version Data: 07070707	
Slot 25, Digital Pin Card OK	
Active Version Data: 04040404	
Low Version Data: 07070707	
ligh Version Data: 07070707	
Slot 26, Digital Pin Card OK	

Low = "07|07|07|07" to represent the 4xLow Channel IC Firmware High = "07|07|07|07" to represent the 4xHigh Channel IC Firmware

Revision History:

Revision Number	Date	Author	Reason For Change
1.0		CW Yu	As published